

03/29/00  
JC685 U.S. PTO

03-30-00

A

**SIDLEY & AUSTIN**  
A PARTNERSHIP INCLUDING PROFESSIONAL CORPORATIONS

CHICAGO  
LOS ANGELES  
NEW YORK  
WASHINGTON, D.C.

717 NORTH HARWOOD  
DALLAS, TEXAS 75201  
TELEPHONE 214 981 3300  
FACSIMILE 214 981 3400

HONG KONG  
LONDON  
SHANGHAI  
SINGAPORE  
TOKYO

FOUNDED 1866

WRITER'S DIRECT NUMBER  
(214) 981-3328

WRITER'S E-MAIL ADDRESS  
pplap@sidley.com

Assistant Commissioner  
for Patents  
Box Patent Application  
Washington, DC 20231

Express Mail Mailing Label No.: EL195378760US

Date of Deposit: MARCH 29, 2000

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the dated indicated above and is addressed to the Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

**DELPHINE NEWELL**

Name of Applicant/Assignee, or Registered Representative

*Delphine Newell*  
Signature

MARCH 29, 2000

Date of Signature

jc586 U.S. PTO  
09/53773  
03/29/00

Re: U.S. Patent Application  
INFORMATION DISPLAY APPARATUS  
By: Yoshifumi SHIBATA, Kiyofumi HASHIMOTO, Eiji  
YAMAKAWA, Hideo HOTOMI, Naoki MASAZUMI, and  
Koichi KOHRIYAMA  
Attorney Docket: 15162/01740

Dear Sir:

Enclosed for filing are the following papers relating to INFORMATION DISPLAY APPARATUS, Yoshifumi SHIBATA, Kiyofumi HASHIMOTO, Eiji YAMAKAWA, Hideo HOTOMI, Naoki MASAZUMI, and Koichi KOHRIYAMA, inventors:

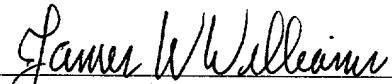
- (1) Specification;
- (2) Unexecuted Declaration and Power of Attorney;
- (3) Formal Drawings (33 sheets);
- (4) Certified Copies of Priority Documents; and
- (5) Check in the amount of \$690.00 to cover the filing fee of the application.

Assistant Commission for Patents  
Box Patent Application  
March 29, 2000  
Page 2

In the event the attached check in the amount of \$690.00 is not received with this correspondence, is not sufficient, or in the event additional fees are due, please charge the required fees (other than issue fee) during the pendency of this application to Deposit Account No. 18-1260. Please credit any overpayment to Deposit Account No. 18-1260.

All correspondence is to be directed to the Applicants' attorney at the Dallas address listed above.

Respectfully submitted,

  
\_\_\_\_\_  
James W. Williams  
Registration No. 20,047  
Attorney for Applicants

JWW/tjf

Enclosures

Attorney Docket No.: 15162/01740

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re

U.S. Application of: Yoshifumi SHIBATA, Kiyofumi HASHIMOTO,  
Eiji YAMAKAWA, Hideo HOTOMI, Naoki  
MASAZUMI, and Koichi KOHIYAMA

For:

INFORMATION DISPLAY APPARATUS

U.S. Serial No. Not yet assigned

Filed: Concurrently

Group Art Unit: To be determined

Examiner: To be determined

Assistant Commissioner

for Patents

Box Patent Application  
Washington, DC 20231

Dear Sir:

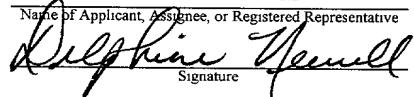
Express Mail Mailing Label No.: EL195378760US

Date of Deposit: MARCH 29, 2000

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the dated indicated above and is addressed to the Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

DELPHINE NEWELL

Name of Applicant, Assignee, or Registered Representative



MARCH 29, 2000

Date of Signature

TRANSMITTAL OF PrintEFS PATENT APPLICATION BIBLIOGRAPHIC DATA

Submitted herewith is bibliographic data (3 pages) for the above-identified application, in the PrintEFS Version 1.0.1 program format.

Respectfully submitted,



James W. Williams  
Registration No. 20,047  
Attorney for Applicants

JWW/tjf

SIDLEY & AUSTIN  
717 N. Harwood  
Suite 3400  
Dallas, Texas 75201-6507  
(214) 981-3328 (direct)  
(214) 981-3300 (main)  
March 29, 2000

INVENTOR INFORMATION

Inventor One Given Name:: Yoshifumi  
Family Name:: SHIBATA  
Postal Address Line One:: Minolta Co., Ltd., Osaka Kokusai Bldg.,  
Postal Address Line Two:: 3-13, 2-Chome, Azuchi-Machi, Chuo-Ku  
City:: Osaka-Shi  
State or Province:: Osaka  
Country:: JAPAN  
Postal or Zip Code:: 541-8556  
City of Residence:: Osaka-Shi  
State or Province of Residence:: Osaka  
Country of Residence:: JAPAN  
Citizenship Country:: JAPAN  
Inventor Two Given Name:: Kiyofumi  
Family Name:: HASHIMOTO  
Postal Address Line One:: Minolta Co., Ltd., Osaka Kokusai Bldg.,  
Postal Address Line Two:: 3-13, 2-Chome, Azuchi-Machi, Chuo-Ku  
City:: Osaka-Shi  
State or Province:: Osaka  
Country:: JAPAN  
Postal or Zip Code:: 541-8556  
City of Residence:: Osaka-Shi  
State or Province of Residence:: Osaka  
Country of Residence:: JAPAN  
Citizenship Country:: JAPAN  
Inventor Three Given Name:: Eiji  
Family Name:: YAMAKAWA  
Postal Address Line One:: Minolta Co., Ltd., Osaka Kokusai Bldg.,  
Postal Address Line Two:: 3-13, 2-Chome, Azuchi-Machi, Chuo-Ku  
City:: Osaka-Shi  
State or Province:: Osaka  
Country:: JAPAN  
Postal or Zip Code:: 541-8556  
City of Residence:: Osaka-Shi  
State or Province of Residence:: Osaka  
Country of Residence:: JAPAN  
Citizenship Country:: JAPAN  
Inventor Four Given Name:: Hideo  
Family Name:: HOTOMI  
Postal Address Line One:: Minolta Co., Ltd., Osaka Kokusai Bldg.,  
Postal Address Line Two:: 3-13, 2-Chome, Azuchi-Machi, Chuo-Ku  
City:: Osaka-Shi  
State or Province:: Osaka  
Country:: JAPAN  
Postal or Zip Code:: 541-8556  
City of Residence:: Osaka-Shi  
State or Province of Residence:: Osaka  
Country of Residence:: JAPAN  
Citizenship Country:: JAPAN

Inventor Five Given Name:: Naoki  
Family Name:: MASAZUMI  
Postal Address Line One:: Minolta Co., Ltd., Osaka Kokusai Bldg.,  
Postal Address Line Two:: 3-13, 2-Chome, Azuchi-Machi, Chuo-Ku  
City:: Osaka-Shi  
State or Province:: Osaka  
Country:: JAPAN  
Postal or Zip Code:: 541-8556  
City of Residence:: Osaka-Shi  
State or Province of Residence:: Osaka  
Country of Residence:: JAPAN  
Citizenship Country:: JAPAN  
Inventor Six Given Name:: Koichi  
Family Name:: KOHIRIYAMA  
Postal Address Line One:: Minolta Co., Ltd., Osaka Kokusai Bldg.,  
Postal Address Line Two:: 3-13, 2-Chome, Azuchi-Machi, Chuo-Ku  
City:: Osaka-Shi  
State or Province:: Osaka  
Country:: JAPAN  
Postal or Zip Code:: 541-8556  
City of Residence:: Osaka-Shi  
State or Province of Residence:: Osaka  
Country of Residence:: JAPAN  
Citizenship Country:: JAPAN

#### CORRESPONDENCE INFORMATION

Name Line One:: James W. Williams  
Name Line Two:: Sidley & Austin  
Address Line One:: 717 N. Harwood  
Address Line Two:: Suite 3400  
City:: Dallas  
State or Province:: Texas  
Country:: USA  
Postal or Zip Code:: 75201-6507  
Telephone One:: 214-981-3328  
Fax One:: 214-981-3400  
Electronic Mail One:: pplap@sidley.com  
Telephone Two:: 214-981-3300  
Electronic Mail Two:: jwilli09@sidley.com

#### APPLICATION INFORMATION

Title Line One:: INFORMATION DISPLAY APPARATUS  
Total Drawing Sheets:: 33  
Formal Drawings?:: Yes  
Application Type:: Utility  
Docket Number:: 15162/01740  
Secrecy Order in Parent Appl.?:: No

REPRESENTATIVE INFORMATION

Registration Number One:: 46284  
Registration Number Two:: 27189  
Registration Number Three:: 37803  
Registration Number Four:: 38425  
Registration Number Five:: 45729  
Registration Number Six:: 45862  
Registration Number Seven:: 30067  
Registration Number Eight:: 44196  
Registration Number Nine:: 44234  
Registration Number Ten:: 45009  
Registration Number Eleven:: 38595  
Registration Number Twelve:: 41341  
Registration Number Thirteen:: 20047

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 11-093007  
Filing Date:: 03-31-1999  
Country:: JAPAN  
Priority Claimed:: Yes  
Foreign Application Two:: 11-161939  
Filing Date:: 06-09-1999  
Country:: JAPAN  
Priority Claimed:: Yes  
Foreign Application Three:: 11-279074  
Filing Date:: 09-30-1999  
Country:: JAPAN  
Priority Claimed:: Yes

Source:: PrintEFS Version 1.0.1

00062600 00062600 00062600

## INFORMATION DISPLAY APPARATUS

This application is based on application Nos. 11-093007, 11-279074 and 11-161939 filed in Japan, the content of which is hereby  
5 incorporated by reference.

## BACKGROUND OF THE INVENTION

### 1. Field of the Invention

10 The present invention relates to an information display apparatus, and more particularly to an information display apparatus which has a display area at least part of which is a liquid crystal display which uses liquid crystal with a memory effect, especially liquid crystal which exhibits a cholesteric phase.

### 15 2. Description of Related Art

In recent years, apparatuses which simultaneously display relatively unchangeable information such as an advertisement and changeable information such as news are demanded. In order to display information which changes at a high speed, a display which can write a  
20 picture thereon at a high speed must be used. Conventional displays of this type are a CRT display and a liquid crystal display which has TFTs (thin film transistors) for the respective pixels so that TN liquid crystal can be driven by an active matrix driving method. In such a display suitable for a motion picture, the whole screen is controlled for writing.  
25 More specifically, the whole screen is scanned in one direction at specified times for writing.

Such a conventional display which is suited to display a motion picture has a disadvantage of consuming great electric power. Also, a liquid crystal display using TFTs is expensive. When the display has a large number of pixels, it is very expensive. With respect to a CRT display, thinning of the display is difficult.

In the meantime, as a PDA or a portable terminal unit such as a portable telephone, a device which is small, light, easy to carry and consumes little battery to be usable for a long time is demanded. At present, however, than the body itself of such a device, its display section and electric source section consume greater electric power, which necessitates the use of a large volume of battery.

Thus, practical use of a display which consumes little electric power is demanded. The present inventors have been developed displays using chiral nematic liquid crystal. Chiral nematic liquid crystal is bistable and has a memory effect. Accordingly, without application of a voltage, such a display can continue displaying a picture thereon, and it is possible to reduce the consumption of electric power. At present, in order to cause this kind of liquid crystal to make a phase transition, a relatively high voltage is necessary; therefore, a simple matrix driving method, not the active matrix driving method using TFTs, is adopted. Because the simple matrix driving method is adopted, even if the display has a large number of pixels, it is not expensive.

When chiral nematic liquid crystal is driven by a simple matrix driving method, however, it takes a relatively long time for writing on the whole screen. It is, therefore, difficult to display a motion picture on the whole screen of such a display when the screen has a large number of

pixels.

## SUMMARY OF THE INVENTION

5        In order to solve the above problems, an object of the present invention is to provide an improved information display apparatus.

Another object of the present invention is to provide an information display apparatus which is suited to display both a motion picture and a still picture.

10        Another object of the present invention is to provide an information display apparatus which can be produced at low cost and consumes little electric power although having a large screen (having a large number of pixels) and is capable of reproducing a motion picture.

15        Further, another object of the present invention is to provide an information display apparatus which can make a display appropriate for the purpose.

20        In order to attain the objects, an information display apparatus according to the present invention comprises: a liquid crystal display having a plurality of liquid crystal pixels defined by a plurality of scan electrodes and a plurality of data electrodes; a driver which is connected to said scan electrodes and said data electrodes to drive said liquid crystal display; and a controller which is connected to said driver, said controller being capable of controlling said driver to select only part of scan electrodes to perform writing on only part of the pixels of the liquid 25 crystal display corresponding to the selected scan electrodes.

Even if the liquid crystal display is a chiral nematic liquid crystal

display which is driven by a matrix driving method, which requires a long time for writing on a large screen, by using only part of the screen, which is divided along the direction of the scan electrodes, for writing, the writing can be completed within a short time, and even a motion picture 5 can be displayed without giving any strange feeling to the observer. For example, if it takes 1msec for writing on one line, by using only 50 lines as a motion picture display area, it takes 0.05sec for writing on the area, which is short enough as a writing time for a motion picture.

Thus, according to the present invention, even if a chiral nematic 10 liquid crystal display driven by a matrix driving method, which requires a relatively long time for writing thereon, is used, by using part of the screen as a motion picture display area and using the other part as a still picture display area, an effective and impressive display can be made. When chiral nematic liquid crystal is used, it takes approximately 1msec 15 for writing on one line, and a large-screen display (with a large number of pixels) can be produced at low cost. Further, because the display has a memory effect, the still picture display area consumes electric power only during writing.

Another information display apparatus according to the present 20 invention comprises: a display having a first display area and a second display area which are different from each other in time required for writing thereon.

In the information display apparatus, since the time required for writing in the first display area and the time required for writing in the 25 second display area are different from each other, it is possible to use these display areas independently depending on the content of

information. For example, a still picture is displayed in either of the display areas which requires a longer time for writing, while information to be written at a high speed such as a motion picture, information inputted by the operator, etc. is displayed in the other area which requires a shorter time for writing. Thus, different types of displays which attain the respective purposes can be made on one screen.

Another information display apparatus according to the present invention comprises: a first display which displays an image by using a first displaying method; and a second display which displays an image by using a second displaying method, said second display being a reflective type liquid crystal display and being capable of keeping the image thereon without consuming electric power.

#### BRIEF DESCRIPTION OF THE DRAWINGS

15

These and other objects and features of the present invention will be apparent from the following description with reference to the accompanying drawings, in which:

Fig. 1 is a schematic structural view of an information display apparatus which is the first embodiment of the present invention;

Fig. 2 is a block diagram which shows a matrix driving circuit of a liquid crystal display employed in the information display apparatus;

Fig. 3 is an illustration which shows display coordinates:

Fig. 4 is an illustration which shows display control:

25 Fig. 5 is an illustration which shows an exemplary picture on the  
display;

Fig. 6 is an illustration which shows an example of displaying motion pictures in a plurality of areas;

Fig. 7 is a sectional view of an exemplary liquid crystal display used as the display;

5 Fig. 8 is a plan view of the liquid crystal display which shows a state wherein a columnar structure and a sealant are formed on a substrate;

Fig. 9 an illustration which shows a manufacturing process of the liquid crystal display;

10 Fig. 10 is a graph which shows the relationship between the voltage of a selective signal applied by the driving circuit and the Y value;

Figs. 11a and 11b are charts which shows voltage waveforms applied to a test cell of the liquid crystal display;

15 Figs. 12a and 12b are charts which show a voltage waveform to drive the liquid crystal display;

Fig. 13 is a block diagram which shows a driving/image processing circuit of the liquid crystal display;

Fig. 14 is a front view of a portable telephone which is the second embodiment of the present invention;

20 Figs. 15a through 15e are illustrations which show a way of displaying information on the portable telephone;

Fig. 16 is a front view of a PDA which is the third embodiment of the present invention;

Fig. 17 is a side view of the PDA;

25 Fig. 18 is a schematic sectional view of a lid of the PDA;

Fig. 19 is a front view of a PDA which is the fourth embodiment of

the present invention;

Fig. 20 is a schematic sectional view of a lid of the PDA;

Fig. 21 is an illustration which shows a picture displayed on the PDA;

5 Figs. 22a through 22d are illustrations which show a way of displaying information on the PDA;

Fig. 23 is an illustration which shows another way of displaying information on the PDA;

10 Fig. 24 is a front view of a mobile type terminal unit which is the fifth embodiment of the present invention;

Figs. 25a and 25b are illustrations which show a way of displaying information on the mobile type terminal unit;

Figs. 26a and 26b are illustrations which show another way of displaying information on the mobile type terminal unit;

15 Fig. 27 is a front view of a watch type terminal unit which is the sixth embodiment of the present invention;

Figs. 28a and 28b are illustrations which show a way of displaying information on the watch type terminal unit;

20 Fig. 29 is a front view of an information display apparatus which is the seventh embodiment of the present invention;

Fig. 30 is a schematic sectional view of the information display apparatus which is the seventh embodiment;

Fig. 31 is a front view of an information display apparatus which is the eighth embodiment of the present invention;

25 Fig. 32 is a schematic sectional view of the information display apparatus which is the eighth embodiment;

Fig. 33 is a schematic sectional view of an information display apparatus which is the ninth embodiment of the present invention;

Fig. 34 is a schematic sectional view of an information display apparatus which is the tenth embodiment;

5 Fig. 35 is a front view of an electronic book which is the eleventh embodiment of the present invention;

Fig. 36 is a front view of an electronic book which is the twelfth embodiment of the present invention;

10 Fig. 37 is a front view of an electronic book which is the thirteenth embodiment of the present invention;

Fig. 38 is a front view of a bulletin board which is the fourteenth embodiment of the present invention;

15 Fig. 39 is a block diagram of a driving section of an information display apparatus which is the fifteenth embodiment of the present invention;

Fig. 40 is a block diagram of the internal circuit of a first scan driver;

Fig. 41 is a chart which shows driving signal waveforms according to a first driving method;

20 Fig. 42 is a block diagram of the internal circuit of a second scan driver;

Fig. 43 is a chart which shows driving signal waveforms according to a second driving method;

Fig. 44 is a block diagram of the internal circuit of a data driver;

25 Fig. 45 is a block diagram of a driving section of an information display apparatus which is the sixteenth embodiment of the present

invention;

Figs. 46a and 46b are illustrations which show a way of displaying information on the information display apparatus which is the sixteenth embodiment; and

5 Fig. 47 is a block diagram of a driving section of an information display apparatus which is the seventeenth embodiment of the present invention.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

10

Some embodiments of information display apparatuses according to the present invention are described with reference to the accompanying drawings.

First Embodiment; See Figs. 1 through 13

15

##### Structure and Control of Information Display Apparatus

Fig. 1 shows an information display apparatus 10 as the first embodiment of the present invention. This apparatus 10 has a liquid crystal display 100 of a matrix control type which is driven by a driving IC 130 controlled by a control circuit 30 by order of a CPU 20. The apparatus 10 further has a video memory 40. The liquid crystal display 100 is three-layered, and each layer is a cell which comprises a substrate with scan electrodes formed thereon, a substrate with data electrodes formed thereon and chiral nematic liquid crystal filled between the substrates. This display 100 displays a full-color image. The structure 20 of the liquid crystal display 100 will be described in detail later. 25

The CPU 20 receives image data and display area data from an

external device such as a personal computer or a communication line and judges the image data whether to be data of a still picture or to be data of a motion picture. Then, the CPU 20 transmits the image data to specified addresses in the video memory 40. The CPU 20 also sends 5 information about the display area and information that the image data are of a still picture or of a motion picture to the control circuit 30.

In the video memory 40, all the information to be displayed on the liquid crystal display 100 is stored. This video memory 40 has addresses corresponding to all the pixels of the liquid crystal display 100. 10 Information about the density and the color of each pixel is stored in the corresponding addresses of the video memory 40 as bit data. The control circuit 30 controls the driving IC 130 in accordance with the information stored in the video memory 40 to write an image on the liquid crystal display 100.

15 The pixels on each layer of the liquid crystal 100, as Fig. 2 shows, are structured by a matrix composed of a plurality of scan electrodes R1, R2 through Rm and a plurality of data electrodes C1, C2 through Cn (n, m: natural numbers). The scan electrodes R1, R2 through Rm are connected to output terminals of a scan electrode driving IC 131, and the 20 data electrodes C1, C2 through Cn are connected to output terminals of a data electrode driving IC 132. As the scan electrode driving IC 131 and the data electrode driving IC 132, conventional ICs comprising a shift register a latch circuit and a switch circuit for voltage application are usable. Each layer of the liquid crystal display 100 has such a pixel 25 structure and such driving ICs.

The scan electrode driving IC 131 sends a selective signal to a

specified one of the scan electrodes R1, R2 through Rm while sending a non-selective signal to the other scan electrodes. The scan electrode driving IC 131 sends the selective signal to the scan electrodes R1, R2 through Rm in order at regular time intervals. In the meantime, the 5 data electrode driving IC 132 sends signals corresponding to image data to the data electrodes C1, C2 through Cn simultaneously for writing on the respective pixels. For example, while a scan electrode Ra is selected (a: natural number,  $a \leq m$ ), the pixels LRa-C1 through LRa-Cn at the intersection of the scan electrode Ra and the data electrodes C1, C2 10 through Cn are simultaneously subjected to writing. In each of the pixels, the voltage difference between the scan electrode and the data electrode is a voltage for writing on the pixel, and writing on each pixel is performed by this writing voltage.

If the threshold voltage to untwist liquid crystal which exhibits a 15 cholesteric phase (first threshold voltage) is  $V_{th1}$ , by applying the voltage  $V_{th1}$  to the liquid crystal for a sufficient time and thereafter dropping the voltage to less than a second threshold voltage  $V_{th2}$  which is lower than the first threshold voltage  $V_{th1}$ , the liquid crystal comes to the planar state. By applying a voltage which is higher than  $V_{th2}$  and lower than 20  $V_{th1}$  for a sufficient time, the liquid crystal comes to the focal-conic state. Each of the states is maintained even after stoppage of application of the voltage. It has been found that such liquid crystal also comes to a state where these two states are mixed, which means that the liquid crystal can make a gray scale display.

25 The control circuit 30 designates combinations of an address in a row direction (scan line) and an address in a column direction (data line)

in order. As Fig. 3 shows, when the control circuit 30 designates addresses A and B, the display coordinate (A, B) which is the combination of these addresses is subjected to writing in accordance with data read from an address in the video memory 40 corresponding to the display 5 coordinate (A, B).

Fig. 4 shows this control in more detail. Here, the liquid crystal display 100 has 1024 pixels in each row (scan line) and 768 pixels in each column (data line). Each of the pixels is capable of displaying one bite of (256) tones or colors. The video memory 40 has 786432 (1024×768) 10 addresses 00000h through CFFFFh, and its capacity is 786432 bites. The liquid crystal display 100 has addresses A0 through A767 in the column direction and addresses B0 through B1023 in the row direction.

When writing is to be performed on the whole screen, the CPU 20 sends a writing address order of 00000h through CFFFFh to the control 15 circuit 30. The control circuit 30 designates these addresses in order as display addresses A and B. An address N in the video memory 40 is converted into display addresses A and B, for example, by using the following expressions. In the expressions, "INT" means taking only the integral part of the calculated value, and "MOD" means taking the 20 surplus of the result.

$$A=INT(N/1024)$$

$$B=MOD(N/1024)$$

Data to be written on display addresses B on a scan electrode corresponding to a display address A are read out from the video memory 25 40 in order and are latched in a latch circuit of the control circuit 30. Then, while controlling the scan electrode driving IC 131 to select the

scan electrode corresponding to the address A, the control circuit 30 controls the data electrode driving IC 132 to write one line in accordance with the latched data.

On the other hand, when writing is to be performed on only part of the screen, the CPU 20 sends a writing address order indicating part of the addresses of the video memory 40 to the control circuit 30. For example, only serial 64 scan lines on the screen are to be subjected to writing, the CPU 20 sends a writing address order of B0000h through BFFFFh to the control circuit 30. Accordingly, while from the scan electrodes R1 through Rm, only 64 scan electrodes corresponding to the lines to be written are selected one by one repeatedly, the data electrode driving IC 132 applies a data signal to the data electrodes selectively in accordance with the data for the selected line repeatedly. Selecting only the 64 lines in order is realized by controlling data supplied to the shift register. With this control, in an area composed of the 64 lines, a motion picture can be displayed.

In this embodiment, the information display apparatus receives motion picture data and also display area data from an external device such as a personal computer, a communication line or the like via an interface 50, and in accordance with the display area data, the writing start line and the writing end line are determined. Needless to say, the display area may be fixed, and in this case, display area data are not sent from the external device or the communication line.

Thus, by determining a writing start line and a writing end line and by writing a motion picture within the area from the writing start line to the writing end line, it is possible to shorten the time intervals

among writing cycles. In the liquid crystal display 100 using chiral nematic liquid crystal, it takes typically approximately 1msec for writing on one line although the time differs depending on the materials of the liquid crystal composition and the driving signal waveform. Accordingly, 5 if writing is performed in an area composed of 64 lines, it takes approximately 64msec for one writing cycle, that is, approximately 15 cycles of writing can be performed per second. When images are written at time intervals in such an extent, they can be seen as a motion picture although slight flickering is recognized.

10        Thus, the number of scan electrodes to be used for a motion picture is limited beforehand to such a value that images written thereon can be recognized as a motion picture. Display area data of a motion picture sent from an external device indicate an area composed of a number of scan electrodes not beyond the limit.

15        Further, in the middle of performing continuous writing on part of the screen, the part of the video memory 40 containing data for the other part of the screen can be updated by the CPU 20. However, the new data are not displayed immediately, and the data are used to write on the whole screen next time.

20        In the above-described control, one bite of data are used for one pixel, but for a display of a full-color image, three bites of data are used for one pixel. In this case, although the volume of data to be stored is three times, other processes are performed in the same way. Otherwise, three memories may be provided for the red, green and blue display 25 layers, respectively. In either case, in displaying a motion picture, the display layers are driven simultaneously, which results in a motion

picture of a high quality without color dislocation.

By performing partial writing control on one screen as described above, for example, a picture shown by Fig 5 can be displayed. A still picture display area which is a larger part of the screen is not subjected to writing so often, and writing on this area is performed at relatively long regular intervals, for example, once a day. On the other hand, a motion picture display area which is a strip-like area in the lower part of the screen receives data from the video memory 40 continuously, and changeable information (news, motion characters, etc.) is displayed in this area.

One screen does not always have only one motion picture display area. As Fig. 6 shows, it is possible to provide a plurality of motion picture display areas on one screen. In this case, the CPU 20 sends data indicating a plurality of writing start lines and a plurality of writing end lines to the control circuit 30. In the above-described address structure, if display area data indicating an area from A8000h to AFFFFh and data indicating an area from C0000h to C7FFFh are sent, motion pictures are displayed in the areas composed of these scanning lines. In this example, each of the motion picture display areas is composed of 32 lines. Accordingly, the total of 64 lines are subjected to continuous writing, and 15 writing cycles are performed per second. In a case of providing a plurality of motion picture display areas on one screen, the total number of scanning lines of all the motion picture display areas shall be within the limit in which images written on the areas can be recognized as motion pictures.

#### Liquid Crystal Display

Next, a liquid crystal display using liquid crystal which exhibits a cholesteric phase is described.

## Structure

Fig. 7 shows an exemplary structure of a liquid crystal display 100.

5 This liquid crystal display 100 is of a reflective type, and has, on a light absorber 121, a red display layer 111R which makes a display by switching between a red selective reflection state and a transparent state.

10 On the red display layer 111R, a green display layer 111G which makes a display by switching between a green selective reflection state and a transparent state is provided, and on the layer 111G, a blue display layer 111B which makes a display by switching between a blue selective reflection state and a transparent state is provided.

15 Each of the display layers 111R, 111G, and 111B has a resin columnar structure 116, liquid crystal 116 and spacers 117 between transparent substrates 112 with transparent electrodes 113 and 114, respectively, thereon. On the transparent electrodes 113 and 114, an insulating layer 118 and an alignment controlling layer 119 are provided if necessary. A sealant 120 is provided on the periphery of the substrates 112 to seal the liquid crystal 116 in the substrates 112.

20 The transparent electrodes 113 and 114 of each display layer are connected to the control circuit 30, and a specified pulse voltage is applied between the electrodes 113 and 114. In response to the voltage applied, the liquid crystal 116 switches between a transparent state wherein the liquid crystal 116 transmits visible light and a selective reflection state 25 wherein the liquid crystal 116 selectively reflects visible light of a specified wavelength, thereby switching a display.

The transparent electrodes 113 and 114 of each display layer are in the form of strips arranged in parallel at slight uniform intervals. The electrode strips 113 and the electrode strips 114 face each other, and the extending direction of the electrode strips 113 and the extending 5 direction of the electrode strips 114 are perpendicular to each other. Electric power is applied between the upper electrode strips and the lower electrode strips in order. Accordingly, in each display layer, a voltage is applied to the liquid crystal 116 in a matrix, so that the liquid crystal 116 makes a display. This is referred to as a matrix drive. By performing 10 this matrix drive toward the display layers sequentially or simultaneously, the liquid crystal display 100 displays a full-color image.

A liquid crystal display with liquid crystal which exhibits a cholesteric phase between two substrates makes a display by switching the liquid crystal between a planar state and a focal-conic state. In the 15 planar state, the liquid crystal selectively reflects light of a specified wavelength  $\lambda = Pn$  ( $P$ : helical pitch of the cholesteric liquid crystal,  $n$ : average refractive index of the liquid crystal). In the focal-conic state, if the wavelength of light selectively reflected is shorter than the infrared spectrum, the liquid crystal transmits visible light. Therefore, by setting 20 the wavelength of light selectively reflected by the liquid crystal within the visible spectrum and providing a light absorbing layer on the side of the display opposite the observing side, the liquid crystal, in the planar state, makes a display of a color corresponding to the wavelength of light selectively reflected and in the focal-conic state, makes a black display. 25 Also, by setting the wavelength of light selectively reflected by the liquid crystal within the infrared spectrum and providing a light absorbing

layer on the side of the display opposite the observing side, the liquid crystal, in the planar state, reflects infrared light and transmits visible light, thereby making a black display, and in the focal-conic state, scatters light, thereby making white display.

5

### Full-color Display

The liquid crystal display 100 which has color display layers 111R, 111G and 111B makes a red display by setting the liquid crystal 116 of the blue display layer 111B and the green display layer 111G to the focal-conic (transparent) state and setting the red display layer 111R to the planar 10 (selective reflection) state. The liquid crystal display 100 makes a yellow display by setting the liquid crystal 116 of the blue display layer 111B to the focal-conic (transparent) state and setting the liquid crystal 116 of the green display layer 111G and the red display layer 111R to the planar (selective reflection) state. By setting the liquid crystal 116 of the 15 respective color display layers to the transparent state or to the selective reflection state appropriately, displays of red, green, blue, white, cyan, magenta, yellow and black are possible. Also, by setting the liquid crystal 116 of the respective color display layers to an intermediate state, displays of intermediate colors are possible. Thus, the liquid crystal 20 display 100 can be used as a full-color display.

The laminating order of the color display layers 111R, 111G and 111B in the liquid crystal display 100 is not limited to the order shown by Fig. 7, and the other orders are possible. However, considering that light in a longer wavelength range is easier to be transmitted than light in a 25 shorter wavelength, it is good to arrange the layer which selectively reflects light of a shorter wavelength in an upper position than the layer

which selectively reflects light of a longer wavelength. With this arrangement, more light transmits downward, and a brighter display becomes possible. Accordingly, it is the best for good display performance to arrange the blue display layer 111B, the green display layer 111G and the red display layer 111R in this order viewing from the observing direction (indicated by arrow "A").

#### Materials for the Display

As the transparent substrates 112, transparent glass plates and transparent resin films can be used. As the transparent resin films, 10 polycarbonate resin, polyether sulfone resin, polyethylene terephthalate resin, norbornene resin, polyallylate resin, amorphous polyolefine resin, modified acrylate resin, etc. can be named. Such resin films used as the transparent substrates 112 are required to have the following characteristics: high light transmittance, optical non-anisotropy, 15 dimensional stability, surface smoothness, antifriction, elasticity, high electric insulation, chemical resistance, liquid crystal resistance, heat resistance, moisture resistance, gas barrier function, etc. A material which has such necessary characteristics is selected depending on the circumstances and the usage.

20 As the transparent electrodes 113 and 114, ITO (indium tin oxide), etc. are usable. Metal electrodes of aluminum, silicon or the like can be used, and photoconductive films of amorphous silicon, BSO (bismuth silicon oxide) or the like can be used. The lowermost electrodes 114 may be preferably black electrodes so as to function as a light 25 absorber.

The insulating layers 118 are inorganic films of silicon oxide or the

like or organic films of polyimide resin, epoxy resin or the like so as to also function as gas barriers. The insulating layers 118 prevent short-circuits among the substrates 112 and improve the reliability of the liquid crystal 116. The alignment controlling layers 119 are typically made of 5 polyimide.

Preferably, the liquid crystal 116 exhibits a cholesteric phase in a room temperature. Chiral nematic liquid crystal which is produced by adding a chiral agent to nematic liquid crystal is especially suited as the liquid crystal 116.

10 A chiral agent is an additive which twists molecules of nematic liquid crystal. When a chiral agent is added to nematic liquid crystal, the liquid crystal molecules form a helical structure with uniform twist intervals, whereby the nematic liquid crystal exhibits a cholesteric phase.

15 By changing the content of the chiral agent in chiral nematic liquid crystal, the pitch of the helical structure can be changed. In this way, the wavelength of light to be selectively reflected by the liquid crystal can be controlled. Generally, the pitch of the helical structure is expressed by a term "helical pitch" which is defined as the distance between molecules which are located at 360° to each other along the 20 helical structure of the liquid crystal molecules.

The columnar structures 115 can be made of, for example, thermoplastic resin. Such thermoplastic resin used for the columnar structures 115 is required to be softened by heat and solidified by cool, not to chemically react to the liquid crystal material used and to have 25 appropriate elasticity.

Specifically, polyvinyl chloride resin, polyvinilidene chloride resin,

00620 00620 00620 00620 00620 00620 00620 00620 00620 00620

polyester methacrylate resin, polyacrylic ester resin, polyvinyl acetate resin, polystyrene resin, polyamide resin, polyethylene resin, polypropylene resin, fluororesin, polyurethane resin, polyacrylonitrile resin, polyvinyl ether resin, polyvinyl ketone resin, polyvinyl pyrrolidone resin, polycarbonate resin, chlorinated polyether resin, saturated polyester resin, etc. can be used.

One of these materials may be used by itself, or a plurality of these materials may be used by mixture. Also, a mixture which at least contains one or more of these materials may be used.

As Fig. 8 shows, such a material is printed into a pattern of dotted columns by a conventional printing method. The size, the arrangement pitch, the shape (cylinder, drum, square pole, etc.) of the columns are determined depending on the size and the image resolution of the liquid crystal display. If the columns are arranged between the electrode strips 113, the actual display area will be large, which is favorable.

The spacers 117 are preferably particles of a rigid material which are not deformed by heat and/or pressure. For example, the spacers 117 are fine glass fiber, balls of silicate glass, inorganic powder such as aluminum powder or spherical particles of an organic synthetic material such as divinylbenzene bridged polymer polystyrene bridged polymer or the like.

By providing the rigid spacers 117 and the resin structure 115 which is of a material which mainly contains thermoplastic polymer and composed of columns arranged in a specified pattern between two substrates 112, the two substrates are wholly supported firmly with the gap maintained evenly. Also, alignment unevenness of the liquid crystal

116 filled therein and an occurrence of bubbles in the liquid crystal 116 under a low temperature can be prevented.

## Exemplary Producing Method of the Liquid Crystal Display

An exemplary producing method of the liquid crystal display 100  
5 is described briefly.

First, on two transparent substrates, a plurality of electrode strips are formed. The electrodes are formed by forming an ITO film on each substrate by sputtering or the like and thereafter patterning the ITO film by photolithography.

10 Next, a transparent insulating layer and a transparent alignment controlling layer are provided on the transparent electrodes of each substrate. The insulating layer and the alignment controlling layer can be formed of an inorganic material such as silicon oxide or an organic material such as polyimide resin by a conventional method such as spin-  
15 coat method or a roll-coat method.

On the alignment controlling layer, usually, a rubbing treatment is not performed. Although the function of the alignment controlling layer has not been cleared yet, the liquid crystal molecules obtain an anchoring effect because of the alignment controlling layer. Thus, degradation of the characteristics of the liquid crystal display with aging can be inhibited. It is possible to add a coloring agent to the materials of these thin layers so as to impart a function as a color filter to these layers, which results in improvements in color purity and contrast.

In this way, transparent electrodes, insulating layers and alignment controlling layers are provided on the two substrates. On one of the substrates, a resin structure is formed on the surface with the

electrodes thereon. For the resin structure, paste of resin material which is prepared by dissolving resin in a solvent is used. A printing method wherein the paste of resin material is extruded from a squeegee onto the substrate placed on a flat plate via a screen, a metal mask or the like, a dispenser method or an ink jet method wherein the paste of resin material is injected from a nozzle onto the substrate and a transfer method wherein the paste of resin material is supplied to a flat plate or a roller and thereafter transferred onto the substrate can be adopted. When the resin structure is formed, the resin structure preferably has a greater height than a desired thickness of the liquid crystal layer.

On the other substrate, a sealant of ultra-violet ray setting resin or thermosetting resin is provided on the surface with the electrodes thereon. The sealant is formed into a ring along the periphery of the substrate. As in the formation of the resin structure, the sealant is formed by a dispenser method or an inkjet method wherein resin is injected from a nozzle onto the substrate, a printing method wherein resin is printed on the substrate via a screen, a metal mask or the like or a transfer method wherein resin is supplied to a flat plate or a roller and thereafter transferred onto the substrate. Next, on at least one of the substrates, spacers are dispersed by a conventional method.

Then, these substrates are laminated with each other and are heated while being pressed from both sides. More specifically, as Fig. 9 shows, for example, the substrate 112a with the resin structure 115 formed thereon is placed on a flat plate 150, and the other substrate 112b is placed on the substrate 112a being pressed by a heating/pressing roller 151 from an end. Thus, the substrates 112a and 112b are heated and

pressed while passing between the roller 151 and the plate 150. By adopting such a method, even if the substrates are flexible substrates such as films, a cell can be fabricated accurately. If the resin structure is thermoplastic polymer, the resin structure is softened by heat and 5 hardened by cool, and the substrates adhere to each other because of the resin structure. If the sealant is thermosetting resin, the heat applied for the lamination of the substrates hardens the resin.

In this laminating process, further, the liquid crystal material is dropped on one of the substrates. The liquid crystal material is spread 10 out between the substrates while the substrates are laminated. In this case, the spacers may be contained in the liquid crystal material, and the liquid crystal material containing spacers is dropped on at least one of the substrates on the surface with the electrodes thereon.

The liquid crystal material is dropped on one of the substrates at 15 an end. Then, while the substrates are laminated pressed by the roller, the liquid crystal material is spread from one end to the other end, and in this way, the liquid crystal material is filled in the whole area between the substrates. By adopting this method of filling the liquid crystal material between the substrates, bubbles which occurred during the 20 laminating process are hardly taken in the liquid crystal material.

The substrates are continuously pressed at least until the substrates are cooled to a temperature less than the softening point of the resin material of the columnar structure, and then, the substrates are released from the pressure. If photosetting resin is used as the sealant, 25 light is radiated thereafter so as to harden the resin.

Using different liquid crystal materials which selectively reflect

light of different wavelengths, respectively, cells for blue, green and red are fabricated in the same way. The fabricated cells are laminated into three layers and are joined by an adhesive. Further, a light absorber is provided on the bottom, and thus, a full-color liquid crystal display is  
5 produced.

#### Other Liquid Crystal Displays

The above-described liquid crystal display 100 has a resin columnar structure inside a liquid crystal display area. Such a structure permits production of a liquid crystal display which is light and has good  
10 display performance although using film substrates. Also, such a liquid crystal display of this structure has various advantages of being easily produced as a large-screen display, of requiring a relatively low driving voltage and of being strong against shock, etc. However, this structure is not necessarily adopted. The liquid crystal display layer may be  
15 structured as a conventional polymer dispersed type liquid crystal composite layer, wherein liquid crystal is dispersed in a polymeric three-dimensional net structure or wherein a polymeric three-dimensional net structure is formed in liquid crystal.

#### First Driving Method

20 In the liquid crystal display 100, the display state of the liquid crystal is a function of the voltage applied and the pulse width. By resetting the whole liquid crystal to the focal-conic state wherein the liquid crystal shows the lowest Y value (luminous reflectance) and thereafter, applying a pulse voltage with a constant pulse width to the  
25 liquid crystal, the display state of the liquid crystal changes as Fig. 10 shows. In the graph of Fig. 10, the y-axis indicates the Y value, and the

x-axis indicates the voltage applied. When a pulse voltage  $V_p$  is applied, the liquid crystal comes to the planar state wherein the liquid crystal shows the highest Y value, and when a pulse voltage  $V_f$  is applied, the liquid crystal comes to the focal-conic state wherein the liquid crystal shows the lowest Y value. Also, when an intermediate pulse voltage between  $V_p$  and  $V_f$  is applied, the liquid crystal comes to an intermediate state between the planar state and the focal-conic state wherein the liquid crystal shows an intermediate Y value, and thus, a display of an intermediate color is possible.

10 Figs. 11a and 11b show waveforms (a) and (b) of pulse voltages applied to a test cell produced by the inventors as a trial. In the experiment, only one pixel was subjected. The voltage of a reset signal was 50V. The pulse width of the reset signal (reset duration) was 200msec in the case of (a) and 50msec in the case of (b). As the selective signal to set the pixel to the planar state, 110V was applied for 5msec. Although the voltage of the selective signal was set to 110V in the experiment, the signal may be of any other voltage. The voltage shall be determined depending on the material and the thickness of the liquid crystal and the pulse width of the signal.

20 In the case of waveform (a) wherein the reset signal was supplied for 200msec, whether the pixel was previously in the planar state or in the focal-conic state, the pixel came to the planar state in good condition by application of the selective signal, and displays of gray scales were possible by changing the voltage of the selective signal. On the other 25 hand, in the case of waveform (b) wherein the reset signal was supplied for 50msec, the reset of the pixel was not always sufficient, and when the

pixel was set to the planar state thereafter, the Y value varied.

As is apparent from this experiment, as the reset signal application time is set longer, the influence of the previous state of the liquid crystal becomes weaker. In other words, by setting the reset signal application time to be sufficiently long, display renewal can be well done without being influenced by the previous state. In the case of waveform (a), the reset signal application time was 200msec, and a display with four tones was possible. If the reset signal is supplied for more time, variations in display state caused by the influence of the previous state will be reduced, and a display with more tones will be possible.

Figs. 12a and 12b show exemplary waveforms of a voltage applied to the liquid crystal according to a first driving method. Fig. 12a shows a waveform (a) to turn off the liquid crystal display 100. First, a pulse voltage of 100V is applied to set the liquid crystal to a homeotropic state, and then, a pulse voltage of 40V is applied. Thereby, the liquid crystal comes to the focal-conic state, and the state is maintained. In this state, the liquid crystal scatters light incident thereto (off state or reset state).

Fig. 12b shows a waveform (b) to turn on the liquid crystal display 20 100. After setting the liquid crystal to the reset state in the above-described way, a pulse voltage of 100V is applied to the liquid crystal for 1msec. Thereby, the liquid crystal comes to the planar state. The liquid crystal keeps in this state after stoppage of the application of voltage and transmits/reflects light incident thereto (on state).

25 Driving/Image Signal Processing Circuit

Fig. 13 shows a driving/image signal processing circuit 220

wherein image data are erasable and written. The liquid crystal display 100 is connected to a scan driver 221 and a data driver 222, which are controlled by a scan controller 223 and a data controller 224, respectively. Image data to be newly displayed are inputted into the data controller 224 from a memory 226, but before the data input, the image data are converted into a selective signal by an image data converter 225.

#### Second Embodiment; See Figs. 14 and 15

The second embodiment is a portable telephone 300 such as a PHS. As Fig. 14 shows, the portable telephone 300 has a display section composed of a first display 301 and a second display 302. The first display 301 is a reflective type liquid crystal display with a memory effect as shown by Fig. 7. The display 301 may be a full-color display composed of three display layers or a monochromatic display with one display layer. The second display 302 is a liquid crystal display which has nematic liquid crystal to be driven in a TN mode. A TFT (thin film transistor) is provided for every pixel so that the liquid crystal display can be driven by an active matrix driving method. The second display 302 requires a shorter time for writing thereon than the first display 301. The second display 302 may be any other type of display as long as it requires a shorter writing time.

Further, the portable telephone 300 has a speaker 303, a menu dial 304 for mode selection/input, a key group 305 for selection of mail, call, manner or function, a ten-key 306, a key group 307 for selection of letter, clear, memory or memo, a microphone 308 and an antenna 309. The functions of these members are well known.

Information is displayed on the first and the second displays 301

and 302 in the following way. When a sentence "Today's meeting starts at 3:00PM in Room A." is inputted by use of the ten-key 306 and the key group 307, this sentence is displayed on the second display 302 as shown by Fig. 15a. Next, when the user enters this input by use of a specified key, the sentence is moved to the first display 301 (see Fig. 15b). Then, when the information is to be revised, for example, if "Room A" is to be changed to "Room B", the cursor is moved to the part of the information to be changed on the first display 301. Then, when a revision is commanded, the part to be changed, that is, "Room A" is displayed on the second display 302 (see Fig. 15c). In this state, an input to replace "A" with "B" is made (see Fig. 15d), and this revised information is entered by use of a specified key (see Fig. 15e). Thereafter, this information is stored in a memory and/or sent as an e-mail through a telephone line if necessary.

15        In the second embodiment, since the first display 301 has a memory effect, the picture displayed thereon is maintained even after a power-off (use-up of a battery). Since the second display 302 requires a short writing time, a new image can be displayed thereon with no delay from a key operation. Accordingly, the inputted content can be seen 20 immediately, which is convenient.

When both the first display 301 and the second display 302 display monochromatic images, it is preferred that the color displayed by the first display 301 is different from the color displayed by the second display 302.

25        Third Embodiment; See Figs. 16 through 18

The third embodiment is a PDA 310. As Figs. 16 and 17 shows,

the PDA 310 has a lid 312 which is connected to a base casing 311 by a hinge 313 to be capable of opening and closing freely. A surface of the lid 312 is a display section. In the display section, a first display 315 and a second display 316 are separately provided. The first display 315 is a 5 reflective type liquid crystal display with a memory effect as shown by Fig. 7, and the second display 316 a liquid crystal display which has nematic liquid crystal to be driven in a TN mode. A TFT (thin film transistor) is provided for every pixel so that the liquid crystal display can be driven by an active matrix driving method. The writing time of the second display 10 316 is shorter than that of the first display 315. The second display 316 may be any other type of display as long as the writing time is short.

In the base casing 311, various keys are provided, and a light pen 317 is encased in such a way to be freely taken out. Further, a portable telephone connecting terminal 318 is connected to the base casing 311.

15 In the third embodiment, inputs and revisions of information are displayed on the second display 316 which requires a shorter writing time. Then, entered information is transmitted and displayed on the first display 315 which has a memory effect. The PDA 310 has an advantage of keep displaying information on the first display 315 even after a 20 power-off, and the second display 316 compensates for the disadvantage of the first display 315 that it takes a long time for writing. As Fig. 18 shows, since a partition area 314 is provided between the first display 315 and the second display 316, the lid 312 is reinforced, which prevents 25 density unevenness of an image on the second display 315 which may be caused by an external stress.

Fourth Embodiment; See Figs. 19 through 23

The fourth embodiment is a PDA which is basically of the same structure as the third embodiment. In Fig. 19, the same members are denoted by the same reference symbols as in Fig. 16. What is different from the third embodiment is that the first display 315 and the second display 316 are arranged in contact with each other. Because the partition area 314 does not exist, the lid 312 is a little weaker. However, because the screens of the displays 315 and 316 are continuously arranged, the user can see the screens easily.

Here, an exemplary way of displaying information in the fourth embodiment is described. In the third embodiment, information is displayed in the same way.

For example, as Fig. 21 shows, a sentence "Meeting on March 9 starts at 1:00PM in Room 1." is displayed on the first display 315 as information to be sent. When the part of the information "1:00PM in Room 1" is to be changed, first the part is designated by use of the light pen 317. Thereby, the part is displayed on the second display 316 (see Fig. 22a). Then, on the second display 316, letters to be changed are designated by use of the light pen 117 (see Fig. 22b). Next, by use of the ten-key on the keyboard or the light pen 317, "Room 1" is changed to "Room 2" (see Fig. 22c). On completion of the revision, the revised information is displayed on the first display 315 (see Fig. 22d).

In the third and fourth embodiments, if the density and/or the contrast of the second display 316 is/are set higher than those of the first display 315, the second display 316 will attract the user's attention. For the same purpose, the second display 316 may be a monochromatic display such as blue. Further, as Fig. 23 shows, the dot size of displayed

letters on the second display 316 may be larger than that of the first display 315, which allows the user to follow the information written at a high speed more easily.

Fifth Embodiment; See Figs. 24 through 26

5        The Fifth embodiment is a mobile type terminal unit 320 to be employed in a global positioning system (GPS). The GPS is a system to clarify the geographic position of the terminal unit 320 by adopting a well-known satellite positioning method. Thereby, the terminal unit 320 is capable of indicating the current position in a map and instructing 10 directions to a destination.

The terminal unit 320, as Fig. 24 shows, has a display section composed of a first display 321 and a second display 322. The first display 321 is a reflective type liquid crystal display with a memory effect as shown by Fig. 7. The second display 322 is a liquid crystal display 15 which has nematic liquid crystal to be driven in a TN mode. A TFT (thin film transistor) is provided for every pixel so that the liquid crystal display can be driven by an active matrix driving method. The writing time of the second display 322 is shorter than that of the first display 321. The second display 322 may be of any other type as long as the writing 20 time is short.

The terminal unit 320 further has a scroll key 323, a key group 324 for inputting an address or the like, a power switch 325, a mode key 326 and an antenna 327. The functions of these members are well known.

25        Here, an exemplary way of displaying information in the fifth embodiment is described. As Fig. 25a shows, on the first display 321, by

use of the GPS, a map indicating the neighborhood of the terminal unit 320 is displayed, and the current position of the unit itself 320 is indicated by a mark "A" on the map. When the address of a destination is inputted by use of the key group 324, the inputted address is displayed 5 on the second display 322 immediately (see Fig. 25b). At the same time, the destination is indicated by a mark "B" in the map displayed on the first display 321, and a route C to the destination is indicated.

Another way of displaying information is described. As Fig. 26a shows, by use of the GPS, a map indicating the neighborhood of the terminal unit 320 is displayed, and the current position of the unit 320 is indicated by a mark “A”. At that time, if the display mode of the device 320 is set in a current position display mode, the address of the position indicated by the mark “A” is displayed as literal information on the second display 322 (see Fig. 26b).

## 15 Sixth Embodiment; See Figs. 27 and 28

The sixth embodiment is a watch type terminal unit 330 to be employed in a GPS. As Fig. 27 shows, the terminal unit 330 has a display section composed of a first display 331 and a second display 332. The first display 331 is a reflective type liquid crystal display with a memory effect as shown by Fig. 7. The second display 332 is a liquid crystal display which has nematic liquid crystal to be driven in a TN mode. A TFT (thin film transistor) is provided for every pixel so that the liquid crystal display can be driven by an active matrix driving method. The writing time of the second display 332 is shorter than that of the first display 331. The second display 332 may be of any other type as long as the writing time is short.

The terminal device 330 further has an exclusive key group 333, a power switch 334, an antenna 335 and a wrist band 336. The functions of these members are well known.

The display section of the sixth embodiment is basically the same as that of the fifth embodiment. Here, a further another way of displaying information on the display section is described. As Fig. 28a shows, when "current position" is inputted on the second display 332, a map and a mark A indicating the current position are displayed on the first display 331. The words "current position" are written on the second display 332 immediately but are erased soon for energy saving. It takes a relatively long time to write a map on the first display 331. Once the map is written, however, it is continuously displayed even after stoppage of supply of electric power, which reduces the consumption of electric power.

As Fig. 28b shows, in a destination input mode, when "E restaurant" is inputted on the second display 332, a mark B indicating the destination and a route C to the destination are displayed in a map on the first display 331. The letters displayed on the second display 332 are erased soon for energy saving. On completion of writing of the map on the first display 331, the supply of electric power is shut off. However, because of the memory effect of the display 331, the map is continuously displayed until erasure is commanded.

#### Seventh Embodiment; See Figs. 29 and 30

The seventh embodiment is an information display apparatus 350A. As Fig. 29 shows, the apparatus 350A has a first display 351 with a relatively large screen and a second display 352 provided in the screen

of the first display 351. The first display 351 is a display using cholesteric liquid crystal or chiral nematic liquid crystal which has a memory effect. The second display 352 is a liquid crystal display which has nematic liquid crystal to be driven in a TN mode. A TFT (thin film transistor) is 5 provided for every pixel so that the liquid crystal display can be driven by an active matrix driving method. The writing time of the second display 352 is shorter than that of the first display 351. The second display 352 may be of any other type as long as the writing time is short. In a casing 10 355, an electric power source, a control section are encased, and on a lower part of the casing 355, a power switch 361 and five selection switches 362 are provided.

Fig. 30 is a sectional view of the apparatus 350A. On the casing 355, the second display 352 and a spacer 356 are arranged, and the first display 351 is arranged thereon. The structure and the driving method 15 of the first display 351 have been described with reference to Fig. 7. The structure and the driving method of the second display 352 are the same as those of a conventional TFT liquid crystal display.

As Fig. 29 shows, the first display 351 displays a relatively fixed still picture, and the second display 352 displays a motion picture which 20 requires high-speed writing. When the second display 352 is to be used, the overlapping part of the first display 351 is set to a transparent state, so that a picture on the second display 352 appears through the transparent part of the first display 351. By turning off the second display 352, it is possible to display a picture written on the first display 25 351 on the whole screen.

A TFT liquid crystal display requires only a short time for writing

and is suited to display a motion picture. It is, however, difficult to produce a TFT liquid crystal display with a large screen, and production of a large-sized TFT liquid crystal display costs much. In the seventh embodiment, therefore, a large screen is made of cholesteric liquid crystal 5 or chiral nematic liquid crystal which can be easily structured into a large-sized display. By using the respective advantages of the displays 351 and 352 and compensating the respective disadvantages of the displays 351 and 352 with each other, shortening of the writing time, enlarging of the screen, reduction of cost and energy saving can be 10 achieved.

#### **Eighth Embodiment; See Figs. 31 and 32**

The eighth embodiment is an information display apparatus 350B which has a first display 351, a second display 352 and a casing 355 which are the same as those of the seventh embodiment. Fig. 31 shows the 15 screen of the information display apparatus 350B.

In the eighth embodiment, however, as Fig. 32 shows, the second display 352 is arranged on the same level with the first display 351. In the eighth embodiment, therefore, when the second display 352 is turned off, it is impossible to use the whole screen of the apparatus 350B. 20 However, because the displays 351 and 352 are arranged separately without being laminated, the apparatus 350B of the eighth embodiment can be produced at lower cost than the apparatus 350A of the seventh embodiment. When the second display 352 is to be used, in the seventh embodiment, the overlapping part of the first display 351 must be set in a 25 transparent state, whereas in the eighth embodiment, it is not necessary to set the first display 351 to a transparent state. Information can be

displayed on the screen of the apparatus 350B in the same ways as on the screen of the apparatus 350A, and the eighth embodiment has substantially the same effects as the seventh embodiment. In the seventh embodiment, when the whole screen is used for a display made by 5 the first display 351, a high quality picture can be displayed because there appears no joints between the displays 351 and 352. In the eighth embodiment, information can be clearly displayed on the second display 352 because the screen of the second display 352 can be seen directly.

Ninth Embodiment; See Fig. 33

10 The ninth embodiment is an information display apparatus 350C which has a first display 351, a second display 352 and a casing 355 which are the same as those of the seventh embodiment. The screen of the apparatus 350C is of the same structure as shown by Fig. 29.

15 As Fig. 33 shows, in the ninth embodiment, however, a transparent resin plate 351a is placed on the second display 352. The usage and the effects of the ninth embodiment are the same as those of the eighth embodiment.

Tenth Embodiment; See Fig. 34

20 The tenth embodiment is an information display apparatus 350D which has a first display 351, a second display 352 and a casing 355 which are the same as those of the seventh embodiment. The screen of the apparatus 350D is of the same structure as shown by Fig. 29.

25 As Fig. 34 shows, in the tenth embodiment, however, the second display 352 is attachable to and detachable from the casing 355 through the reverse side of the casing 355 together with a board 357 with a control section therein. Accordingly, in the tenth embodiment, it is possible to

detach the second display 352 from the casing 355 so as to display information on the second display 352 independently of the first display 351. The usage and the effects of the tenth embodiment are the same as those of the seventh embodiment.

5 The attaching/detaching mechanism of the second display 352 may be of any structure. For example, as Fig. 34 shows, a holder protrusion 358 and a slider 359 are provided on the reverse side of the casing 355, and the second display 352 is attached to or detached from the casing 355 as the slider 359 slides right and left.

## 10 Eleventh Embodiment; See Fig. 35

The eleventh embodiment is an electronic book 370A. On a casing 375, a first display 371 and a second display 372, a power switch 376 and a plurality of operation buttons 377 are provided. The first display 371, as in the seventh through tenth embodiment, is a reflective type liquid crystal display using cholesteric liquid crystal or chiral nematic liquid crystal which has a memory effect. The second display 372 is an organic EL (electro luminescence) display. The organic EL display is of a light emitting type and requires only a very short time for writing.

20 The first display 371 and the second display 372 are laminated as shown by Fig 30, and the second display 372 is located under the first display 371. Information is displayed on the first and second displays 371 and 372 in the ways described in connection with the seventh embodiment.

25 In the eleventh embodiment, the first display 371 displays literal information of a book by using its memory effect. The second display 372

is used in a rapid mode to perform paging for skimming by using its short writing time, and a motion picture can be displayed on the second display 372.

In the eleventh embodiment, as the second display 372, any type of display which requires only a short time for writing can be used as well as an organic EL display. For example, the second display 372 may be a PDP (plasma display panel), an FED (field emission display), an LED (light emitting diode) or a FIL (fluorescent indicator lamp). The second display 372 also may be a TFT liquid crystal display.

## 10 Twelfth Embodiment; See Fig. 36

The twelfth embodiment is an electronic book 370B which is of the same structure as the eleventh embodiment. The electronic book 370B has a large-screen first display 371 and two small-screen second displays 372a and 372b. The second displays 372a and 372b are of a type which requires only a short time for writing, and may be TFT liquid crystal displays, organic EL displays, PDPs, FEDs, LEDs, FILs or the like.

In the twelfth embodiment, information are displayed on the three displays 371, 372a and 372b in different ways. The respective writing times of the second displays 372a and 372b are shorter than that of the first display 371, and the writing time of the second display 372a is shorter than that of the second display 372b.

### Thirteenth Embodiment; See Fig. 37

The thirteenth embodiment is an electronic book 380 which has a spreadable screen for two pages. Specifically, the electronic book 380 has two panels 385a and 385b and is foldable between the panels 385a and 385b. The left panel 385a comprises a reflective type liquid crystal

display 381 using cholesteric liquid crystal or chiral nematic liquid crystal which has a memory effect, a power switch 383, a plurality of operation buttons 384 and a control section thereof. The right panel 385b comprises a second display 382 and a control section thereof. The second display 382 is a liquid crystal display which has nematic liquid crystal to be driven in a TN mode. A TFT (thin film transistor) is provided for every pixel so that the liquid crystal display can be driven by an active matrix driving method. The writing time of the second display 382 is shorter than that of the first display 381. The second display 382 may be 10 of any other type as long as the writing time is short.

In the thirteenth embodiment, the first display 381 has a larger screen than the second display 382. The first display 381 and the second display 382 are used in the same ways as described in connection with the tenth embodiment. The first and second displays 381 and 382 display 15 information using the respective advantages. The second display 382 may be a display of a light emitting type which requires a short time for writing, such as an EL display, a PDP, a FED, an LED, a FIL or the like.

#### Fourteenth Embodiment; See Fig. 38

The fourteenth embodiment is a bulletin board 390. On a casing 20 395, a large-screen first display 391 for displaying letters and a small-screen second display 392 for displaying an image. The first display 381, like the first display 371 in the eleventh embodiment, is a reflective type liquid crystal display using cholesteric liquid crystal or chiral nematic liquid crystal which has a memory effect. The second display 392, like 25 the second display 372 in the eleventh embodiment, is an organic EL display. The first display 371 and the second display 372 are arranged

as shown by Fig. 32 or 33 so as not to overlap.

Generally, an image contains a larger volume of information than a group of letters, and the observer can recognize the content of the information easily from an image. Therefore, when literal information 5 and image information are to be displayed on one screen, the following operation is preferable: while image information is fed forward rapidly on the second display 392, an image to be displayed is determined; and thereafter, literal information describing the image is displayed on the first display 391.

10 The second display 392 may be a TFT liquid crystal display.

#### Fifteenth Embodiment; See Figs. 39 through 44

In the fifteenth, sixteenth and seventeenth embodiments below, a display section is composed of a reflective type liquid crystal display with a memory effect as shown by Fig. 7, and part of the display section is 15 driven by a second driving method wherein a driving voltage composed of reset pulses to reset the liquid crystal, selecting pulses to select desired display states of the respective pixels and maintaining pulses to fix the selected states of the pixels is applied to the liquid crystal. The other part is driven by the above-described first driving method. In the first 20 driving method, an image with three or more tones can be displayed, but it takes a long time for writing. In the second driving method, although only a two-tone image is displayed, but it takes a relatively short time for writing.

Fig. 39 shows a driving section of the fifteenth embodiment. The 25 liquid crystal display 100 is divided into a first display area 251 and a second display area 252 and is driven by a first scan driver 221a, a second

scan driver 221b and a data driver 222. The first scan driver 221a drives the first display area 251 in the first driving method, and the second scan driver 221b drives the second display area 252 in the second driving method.

5 Fig. 40 shows the internal circuit of the first scan driver 221a. The first scan driver 221a comprises a shift register 260, a latch circuit 261, a NAND circuit 262 and a driver 263 with a high withstand voltage. To the circuit, a polarity conversion signal PC, a latch strobe signal STB, a data signal DATA, a shift clock signal CLK and a register clear signal 10 CLR are inputted.

Fig. 41 shows waveforms of scan signals and a data signal according to the first driving method. Signals of the waveforms 401, 402 and 403 are applied to a first scan electrode, a second scan electrode and a third scan electrode, respectively, and these signals are applied to all the 15 scan electrodes in the first display area 251 in order. A signal of the waveform 403 is applied to the data electrodes in synchronization with the application of signals of the waveforms 400, 401 and 402. A driving duration is composed of a reset period 410, a pause period 411, a reset period 412, a pause period 413 and selection periods 414, 415 ... .

20 In the first scan driver 221a, the data signal DATA is set in the shift register 260 in synchronization with the shift clock signal CLK, and when the latch strobe signal STB is inputted, the data are latched in the latch circuit 261. In accordance with the latched data and the polarity conversion signal PC, a signal VPP or a signal VNN is outputted from the 25 driver 263. Table 1 is a truth table of the first scan driver 221a.

Table 1

DATA1	PC	Output
L	H	VNN
H	H	VPP
L	L	VPP
H	L	VNN

In order to drive the first display area 251 in the first driving method, the outputs VPP and VNN from the driver 263 are switched 5 among V1, V2, GND, -V1 and -V2 in the reset period 410, the pause period 411, the reset period 412, the pause period 413 and the selection periods 414, 415, 416 . . . In the following, the times to switch the outputs are described.

In the reset period 410, the outputs VPP and VNN from the driver 10 263 are set to V1 and -V1, respectively. Thereby, in the reset period 410, a pulse signal of voltages V1 and -V1 is applied to the liquid crystal in the first display area 251.

In each of the pause periods 411 and 413, the outputs VPP and VNN from the driver 263 are set to V1 and GND, respectively. Thereby, 15 in the pause periods 411 and 413, a signal of a GND level is applied to the liquid crystal in the first display area 251.

In the reset period 412, the outputs VPP and VNN from the driver 263 are set to V2 and -V2, respectively. Thereby, in the reset period 412, a pulse signal of voltages V2 and -V2 is applied to the liquid crystal of the 20 first display area 251.

In each of the selection periods 413 through 416, the outputs VPP and VNN from the driver 263 are set to V1 and -V1, respectively. Thereby, in the selection periods 413 through 416, a pulse signal of

voltages  $V_1$  and  $-V_1$  is applied to the liquid crystal in the first display area 251.

Fig. 42 shows the internal circuit of the second scan driver 221b. The internal circuit comprises a shift register 500, a latch circuit 501, a 5 decoder 502 and a level shifter/seven-value driver 503 with a high withstand voltage. Into the circuit, a polarity conversion signal PC, a latch strobe signal STB, a two-bit data signal DATA, a shift clock signal CLK and a register clear signal CLR are inputted.

Fig. 43 shows waveforms of scan signals and a data signal 10 according to the second driving method. Signals of the waveforms 700, 701 and 702 are applied to a first scan electrode, a second scan electrode and a third scan electrode, respectively, and these signals are applied to all the scan electrodes in the second display area 252 in order. A signal of the waveform 703 is applied to the data electrodes in synchronization 15 with the application of signals of the waveforms 700, 701 and 702. A driving duration is composed of a reset period 710, a selection period 711 and a maintenance period 712. The signals of the waveforms 700, 701 and 702 start to be applied at lags of a one-line scan time. As is apparent 20 from these waveforms, in the second driving method, it is necessary to output different voltages  $V_1$ ,  $V_2$  and  $V_3$  or  $-V_1$ ,  $-V_2$  and  $-V_3$  simultaneously. In order to comply with this, the second scan driver 221b has a driver 503 which outputs seven different voltages  $V_1$ ,  $V_2$ ,  $V_3$ ,  $-V_1$ ,  $-V_2$ ,  $-V_3$  and GND.

In the second scan driver 221b, the two-bit data signal DATA is set 25 in the shift register 500 in synchronization with the shift clock signal CLK, and when the latch strobe signal STB is inputted, the data are

latched in the latch circuit 501. The latched two-bit data are decoded by the decoder circuit 502 in accordance with the polarity conversion signal PC. Then, from the level shifter/driver 503, any specified voltage V1, V2, V3, -V1, -V2, -V3 or GND is outputted. Table 2 is a truth table of the 5 second scan driver 221b.

Table 2

DATA1	DATA2	PC	Output
L	L	H	GND
L	H	H	V1
H	L	H	V2
H	H	H	GND
L	L	L	GND
L	H	L	-V1
H	L	L	-V2
H	H	L	GND

Fig. 44 shows the internal circuit of the data driver 222. The 10 internal circuit comprises a shift register 800, a latch circuit 801, a comparator 802, a decoder circuit 803, a level shifter/three-value driver 804 with a high withstand voltage and a counter 805. Into the circuit, an output inhibiting signal OE, a polarity conversion signal PC, a latch strobe signal STB, an eight-bit data signal DATA, a shift clock signal CLK, 15 a register clear signal CLR, a counter clock signal CLK and a counter clear signal CCLR are inputted.

In the data driver 222, the eight-bit data signal DATA is set in the shift register 800 in synchronization with the shift clock signal CLK, and when the latch strobe signal STB is inputted, the data are latched in the 20 latch circuit 801. Then, when the count clock signal CCLK is inputted to

the counter 805, a count-up process is performed. More specifically, the comparator 802 compares the output from the latch circuit 801 with the output from the counter 805. When the output from the latch circuit 801 is larger, the comparator outputs a HI level signal, and when the output 5 from the latch circuit is smaller, the comparator outputs a LOW level signal. The decoder 803 receives the output from the comparator 802, the output inhibiting signal OE and the polarity conversion signal PC and outputs a voltage of VPP, VNN or GND to drive the level shifter/driver 804. Table 3 is a truth table of the data driver 222.

10

Table 3

Output from Comparator	PC	OE	Output
Indefinite	Indefinite	H	GND
L	L	L	-V4
H	L	L	V4
L	H	L	V4
H	H	L	-V4

In the fifteenth embodiment, the first display area 251 of the liquid crystal display 100 is connected to the first scan driver 221a which 15 drives the first display area 251 in the first driving method, and the second display area 252 is connected to the second scan driver 221b which drives the second display area 252 in the second driving method. Accordingly, in the first display area 251, an image is written by the first driving method, and in the second display area 252, an image is written 20 by the second driving method.

In the first driving method and in the second driving method, liquid crystal moves differently, and it may be necessary to set the data

pulse signal applied to the first display area 251 from the data driver 222 and the data pulse signal applied to the second display area 252 from the data driver 222 to be of mutually different voltages. In this case, in driving the first display area 251, the outputs VPP and VNN from the 5 data driver 222 are set to V4 and -V4, respectively, and in driving the second display area 252, the outputs VPP and VNN from the data driver 222 are set to V5 and -V5, respectively.

In the first driving method, although full-color display is possible, the time required for writing on one line is approximately 3ms. On the 10 other hand, in the second driving method, the time required for writing on one line is approximately 0.3msc. In short, in the second driving method, writing is performed approximately 10 times as fast as in the first driving method. In order to make a full-color display in the second driving method, however, the thickness of the liquid crystal layer must be even 15 with very high accuracy, and to produce such a liquid crystal display is very difficult. In order to make a full-color display by turning on and off each of the liquid crystal layers, the requirement to the thickness of the liquid crystal layers is not so accurate, and this is easily realized. In consideration for the above points, in the fifteenth embodiment, an image 20 to be displayed at full color is written in the first display area 251 and an image to be displayed at a high speed is written in the second display area 252.

#### Sixteenth Embodiment; See Figs. 45 and 46

The above-described second scan driver 221b is capable of 25 outputting seven different voltages V1, V2, V3, -V1, -V2, -V3 and GND, and by using five of them, namely, V1, V2, -V1, -V2 and GND, the second

scan driver is capable of executing the first driving method.

In the sixteenth embodiment, as Fig. 45 shows, all the scan electrodes of the liquid crystal display 100 are driven by the second driver 221b. The liquid crystal display 100 is divided into an upper first display area 251a, a lower first display area 251b and a second display area 252 which is located between the upper and lower first display areas 251a and 251b. The second display area 252 is driven by the second driving method, and the first display areas 251a and 251b are driven by the first driving method. The reverse is also possible. In the sixteenth embodiment, any desired scan electrodes can be driven in either of the first driving method and the second driving method, and thus, a first display area and a second display area can be set arbitrarily.

In changing the image on the display 100, first, the whole screen is subjected to writing by the first driving method, and a part to be changed (here, the second display area 252) is subjected to writing by the second driving method. Also, it is possible to perform writing by the second driving method on the whole screen first and to perform writing by the first driving method on a part to be changed (the second display area 252). In the first driving method, it takes a long time for writing, but a full-color display is possible. In the second driving method, a full-color display is difficult, but it takes only a short time for writing.

Writing on the part to be changed can be performed not only once but several times repeatedly after writing on the whole screen. As Figs. 46a and 46b show, a menu window 253 opens on the screen, and it is possible to change the content displayed in the window 253 serially. At this time, if the information to be serially displayed in the window 253

does not have to be displayed at full color, for example, if the information is of a text type, this part shall be driven by the second driving method, which results in making a high-speed display.

Seventeenth Embodiment; See Fig. 47

5        In the seventeenth embodiment, as in the fifteenth and sixteenth embodiments, part of the liquid crystal display is driven by the first driving method, and the other part is driven by the second driving method. As Fig. 47 shows, the seventeenth embodiment comprises a first scan driver 221a, a second scan driver 221b and data drivers 222a and 222b.

10       What are different from the fifteenth embodiment are that the data electrodes are divided into a first display area 251 and a second display area 252 and that the display areas 251 and 252 are connected to the respective data drivers 222a and 222b. In this structure, the first display area 251 and the second display area 252 are driven by circuits 15 which are completely independent of each other. Accordingly, data signal waveforms of different voltages can be applied to the liquid crystal display 100, and the first and second display areas 251 and 252 can be scanned simultaneously. Also, a crosstalk, which is a problem that a data signal is applied to unselected scan electrodes, can be avoided.

20       Because the data electrodes are divided, the display areas 251 and 252 does not interfere with each other.

Although the present invention has been described in connection with the preferred embodiments above, it is to be noted that various changes and modifications are possible to those who are skilled in the art. 25 Such changes and modifications are understood as being within the present invention.

WHAT IS CLAIMED IS:

1. An information display apparatus comprising:
  - a liquid crystal display having a plurality of liquid crystal pixels defined by a plurality of scan electrodes and a plurality of data electrodes;
  - a driver which is connected to said scan electrodes and said data electrodes to drive said liquid crystal display; and
  - a controller which is connected to said driver, said controller being capable of controlling said driver to select only part of scan electrodes to perform writing on only part of the pixels of the liquid crystal display corresponding to the selected scan electrodes.
2. The information display apparatus according to claim 1, wherein said liquid crystal display comprises a liquid crystal material which is capable of keeping one of a plurality of stable states under no field conditions.
3. The information display apparatus according to claim 2, wherein said liquid crystal material exhibits a cholesteric phase in a room temperature.
4. The information display apparatus according to claim 1, wherein said controller controls said driver based on motion picture data.
- 25 5. The information display apparatus according to claim 1, wherein said controller sends data regarding a writing start line and a writing end

line to said driver.

6. An information display apparatus comprising:

a display having a first display area and a second display area

5 which are different from each other in time required for writing thereon.

7. The information display apparatus according to claim 6, wherein  
said first display area and the second display area are driven by a first  
driving method and a second driving method, respectively.

10

8. The information display apparatus according to claim 6, wherein  
said first display area is capable of displaying an image with a first  
contrast, and said second display area is capable of displaying an image  
with a second contrast.

15

9. The information display apparatus according to claim 6, wherein  
said first display area is capable of displaying an image with three or  
more tones, and said second display area is capable of displaying an  
image with two tones.

20

10. The information display apparatus according to claim 6, wherein  
said first display area and said second display area display images with  
mutually different dot sizes, respectively.

25

11. An information display apparatus comprising:

a first display which displays an image by using a first displaying

method; and

5 a second display which displays an image by using a second displaying method, said second display being a reflective type liquid crystal display and being capable of keeping the image thereon without consuming electric power.

12. The information display apparatus according to claim 11, wherein a display area of said second display is larger than that of said first display.

10

13. The information display apparatus according to claim 11, wherein said second display has a liquid crystal material which exhibits a cholesteric phase at a room temperature.

15

14. The information display apparatus according to claim 11, wherein said first display and said second display overlap each other.

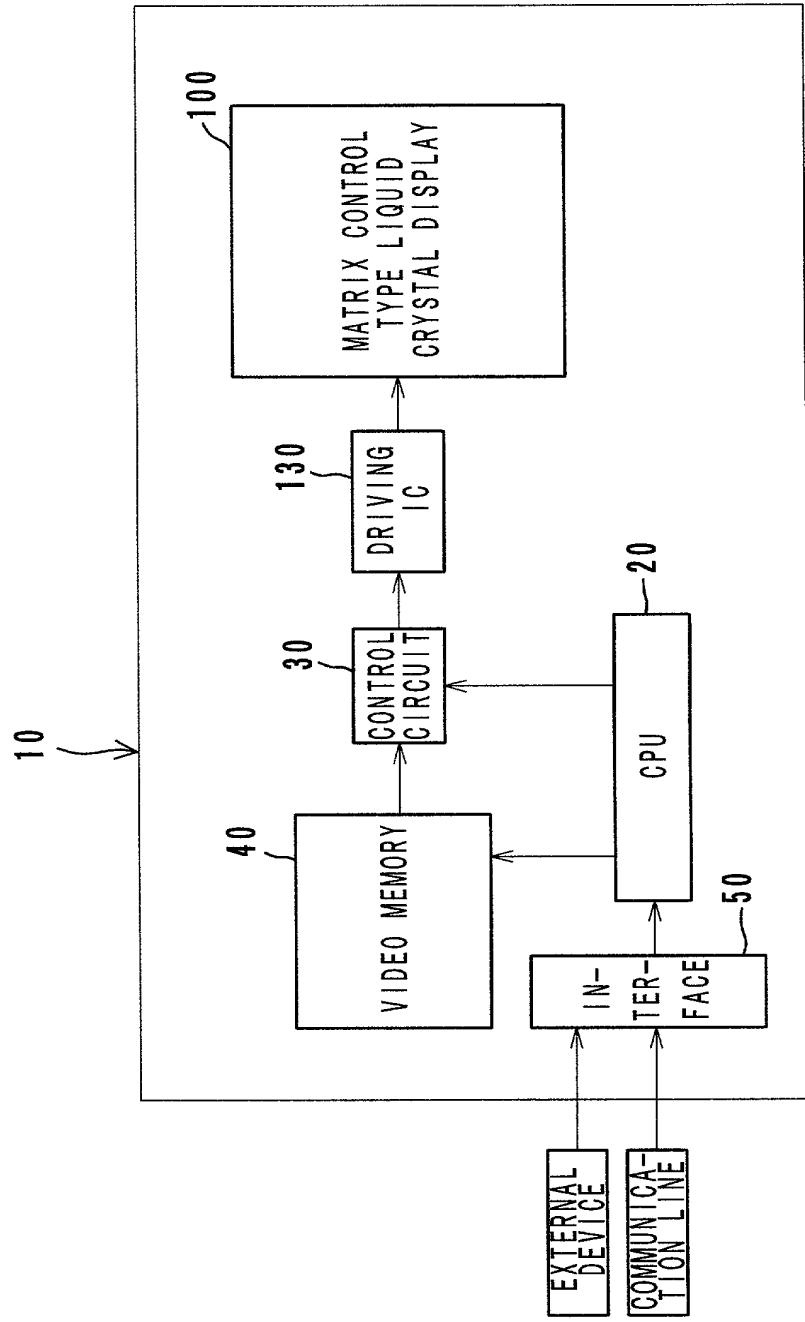
15. The information display apparatus according to claim 11, wherein said first display is detachable from said information display apparatus.

00052000 0000000000

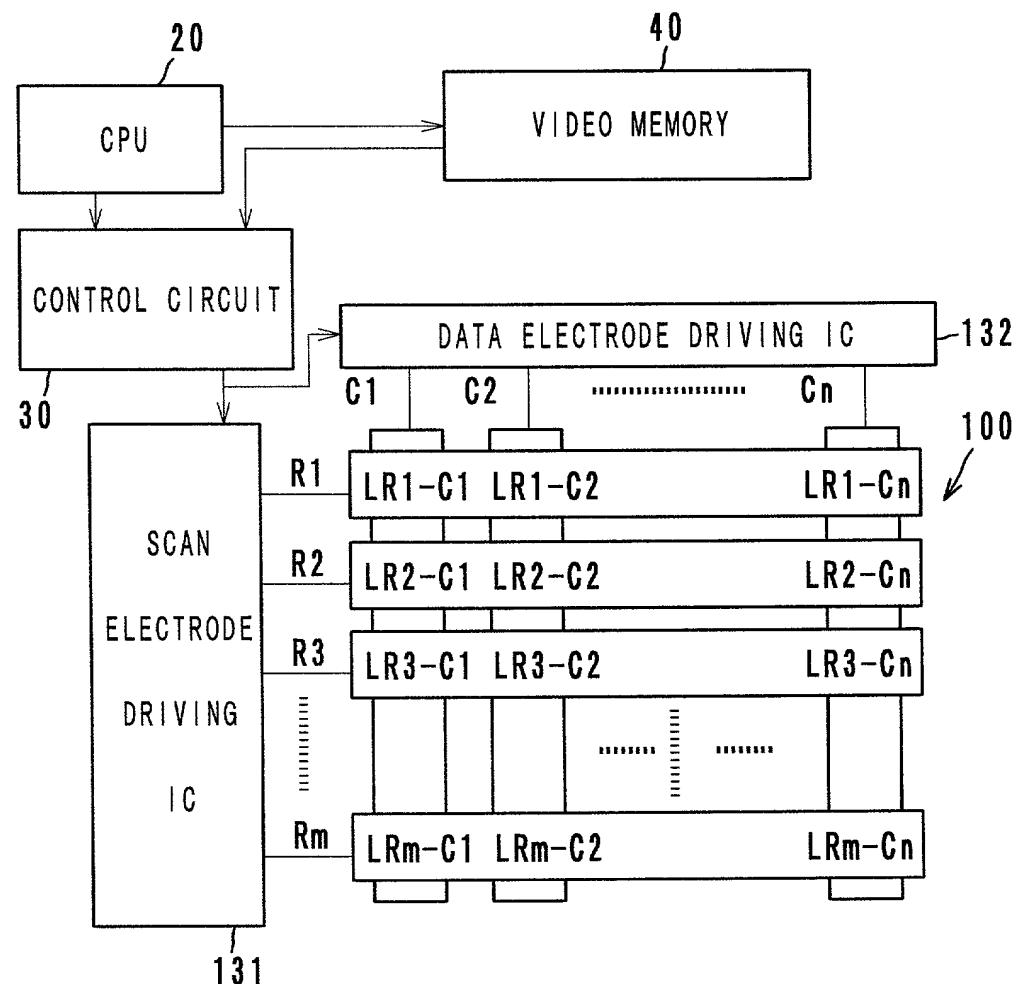
## ABSTRACT OF THE DISCLOSURE

An information display apparatus which has a liquid crystal display which uses chiral nematic liquid crystal with a memory effect and  
5 can be driven by a matrix driving method. The screen of the display is divided along a scanning line into a still picture display area and a motion picture display area. The information display apparatus may have a first display and a second display on one screen. The first display is a reflective type liquid crystal display with a memory effect, which has an  
10 advantage of saving energy, and is used exclusively to display a still picture. The second display is a liquid crystal display which requires only a short time for writing. Information to be written in a short time, such as inputted information, is displayed on the second display.

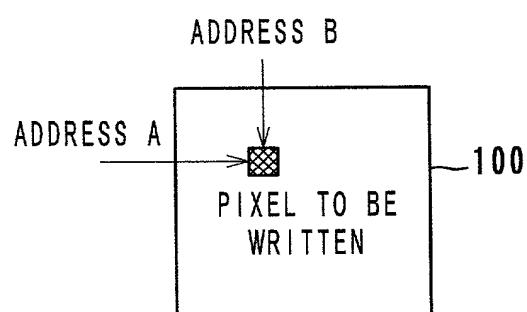
F / G. 1



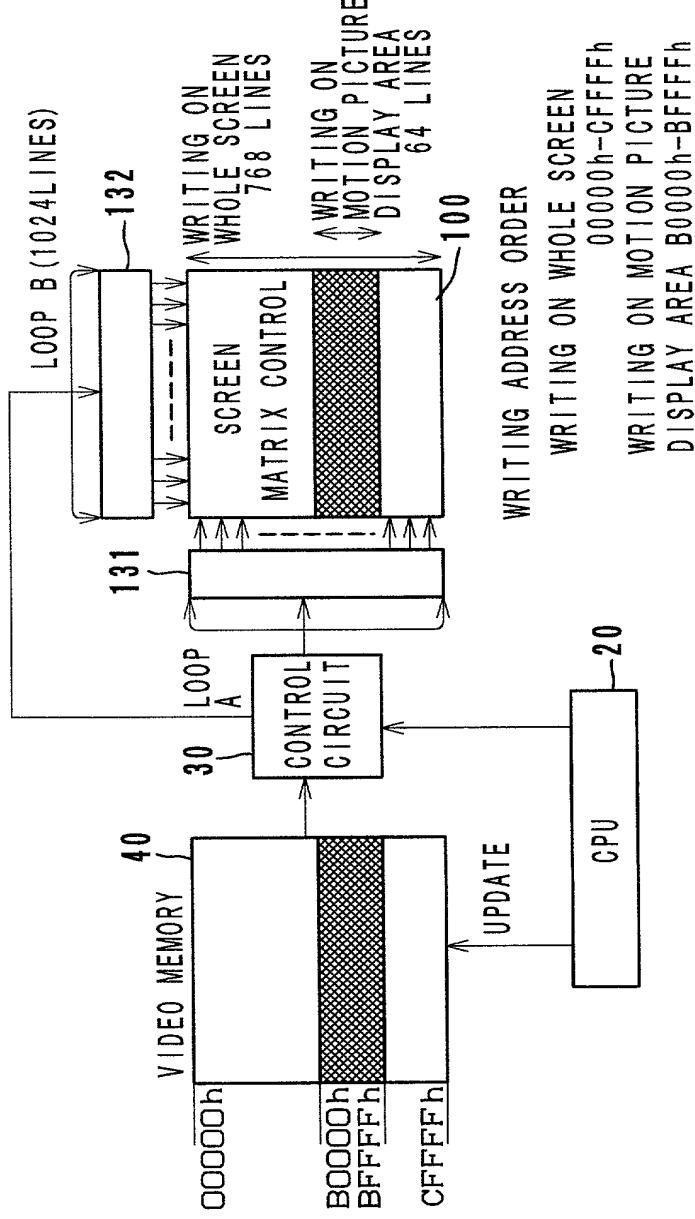
F / G. 2



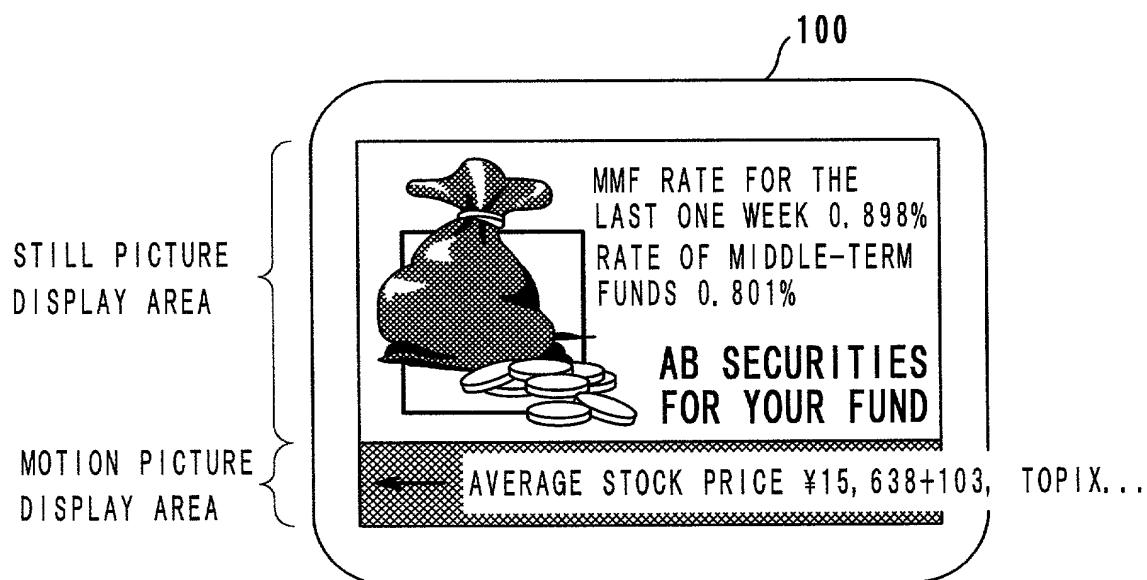
F / G. 3



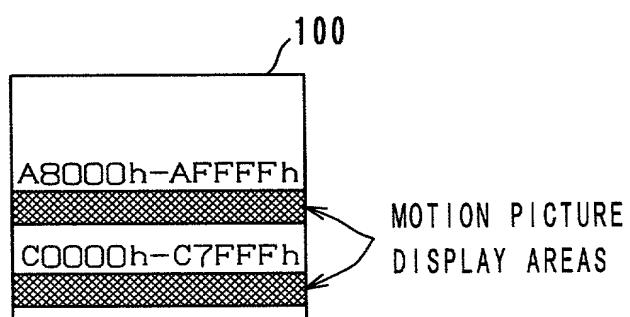
F / G. 4



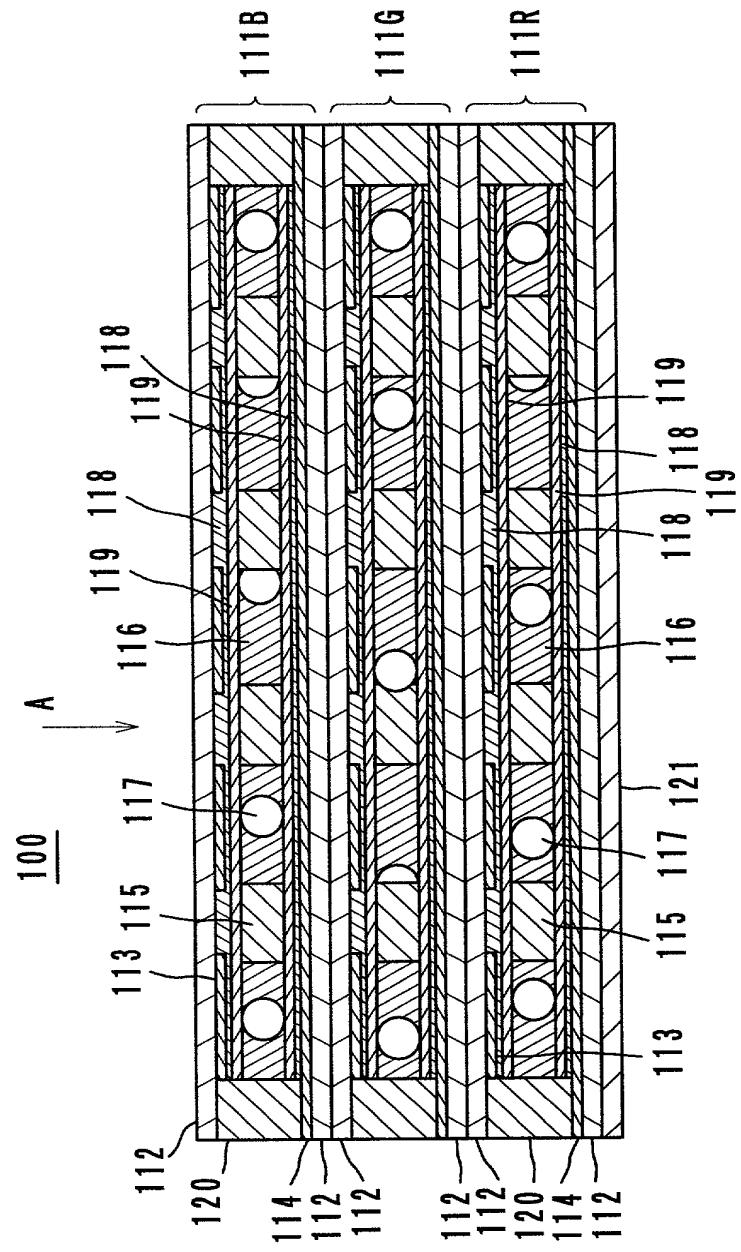
F / G. 5



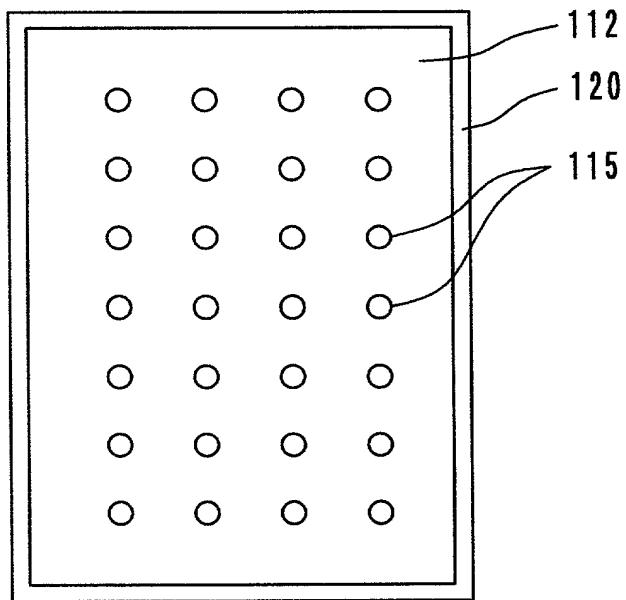
F / G. 6



F / G, 7



F / G. 8



F / G. 9

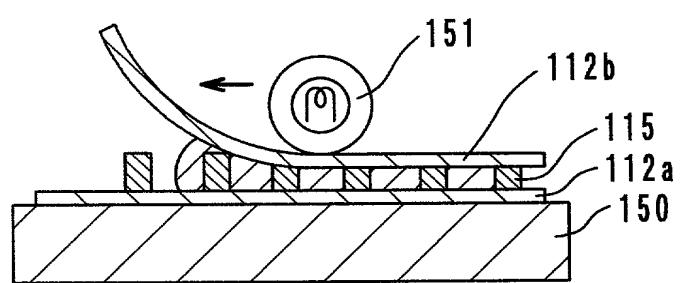


FIG. 10

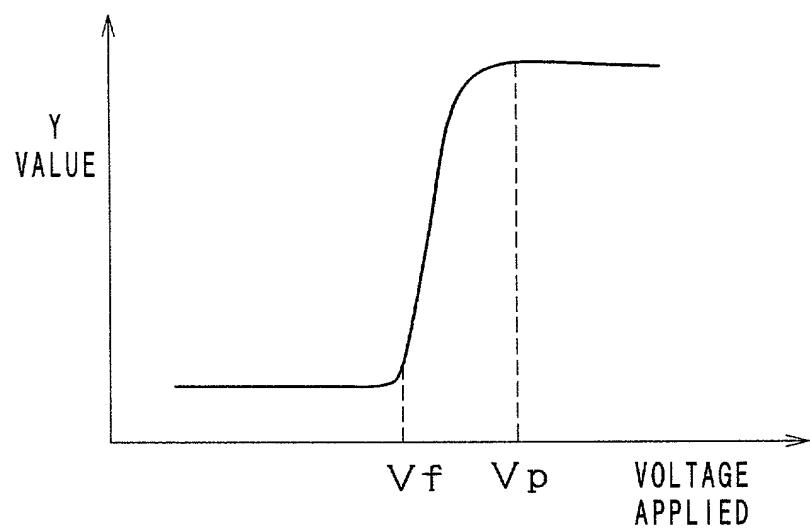
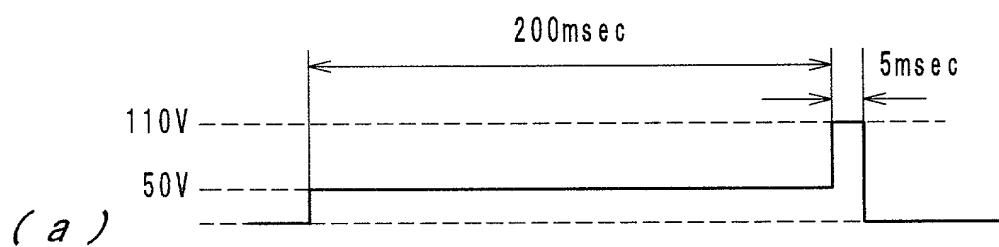
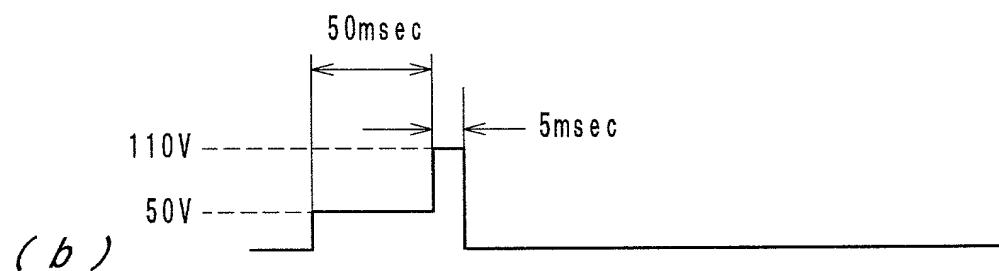


FIG. 11a



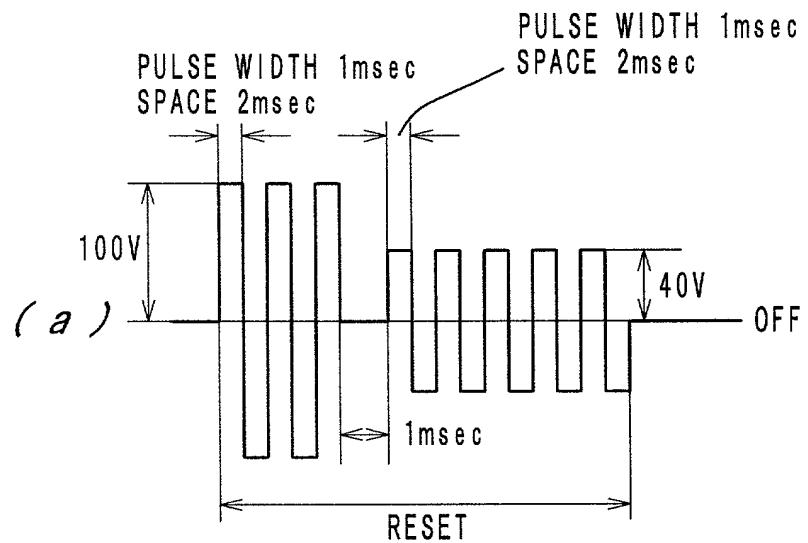
(a)

FIG. 11b

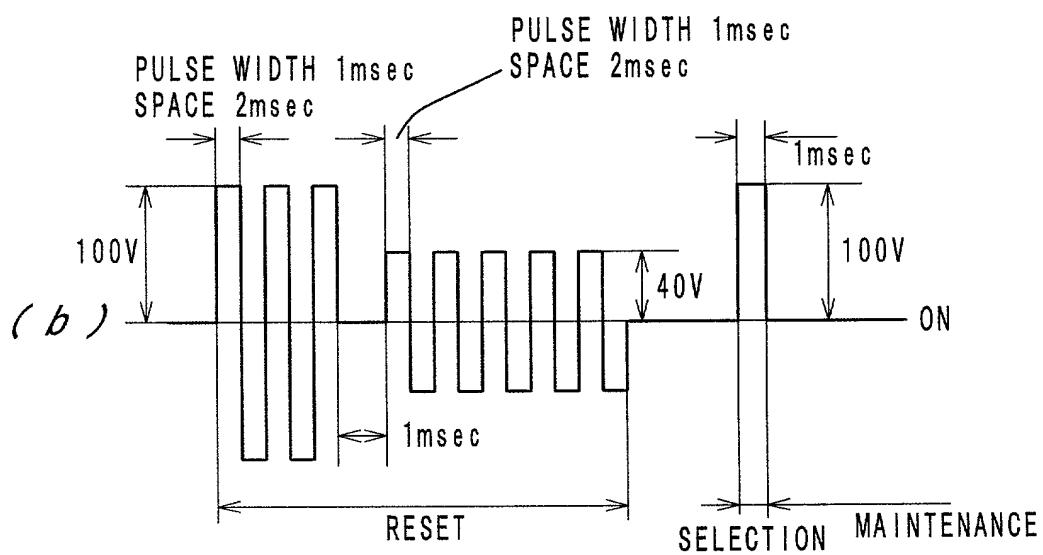


(b)

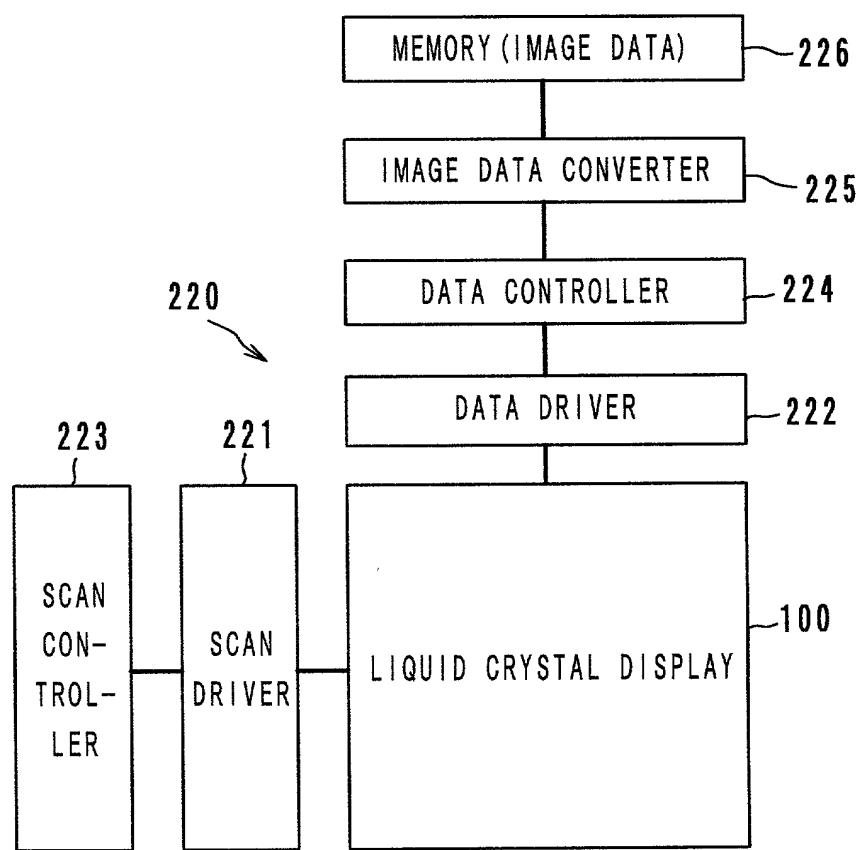
F / G. 12 a



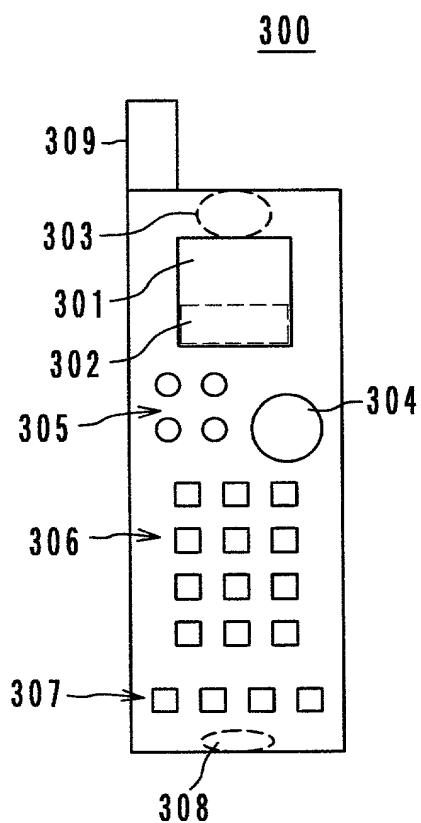
F / G. 12 b



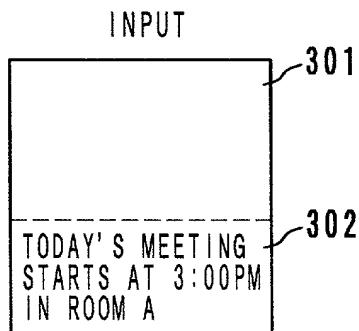
F / G. 13



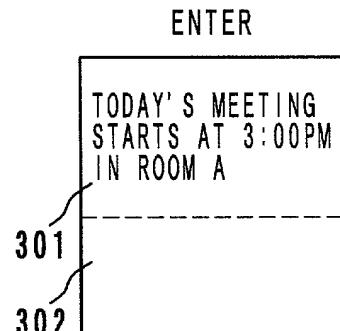
F / G. 14



F / G. 15 a

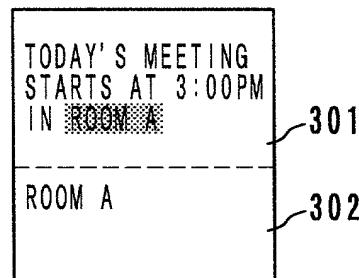


F / G. 15 b



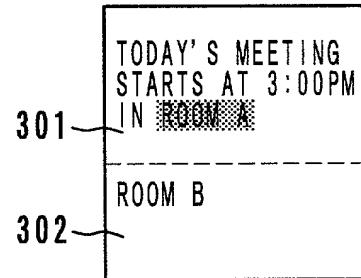
F / G. 15 c

DESIGNATION OF PART  
TO BE CHANGED



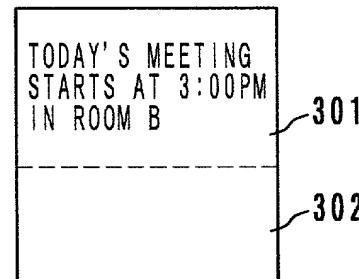
F / G. 15 d

REVISION



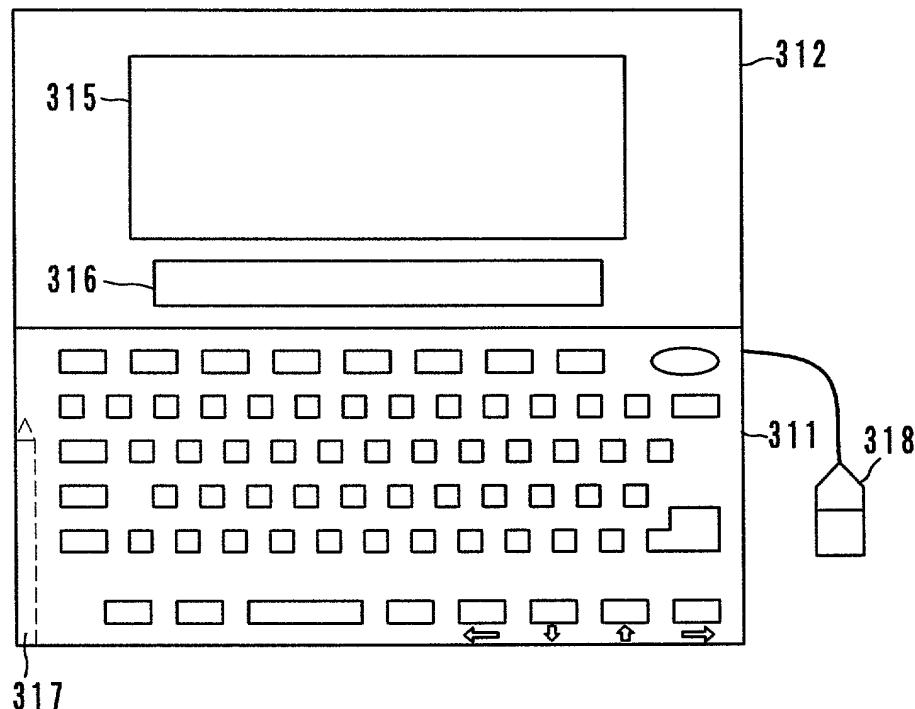
F / G. 15 e

COMPLETION OF  
REVISION



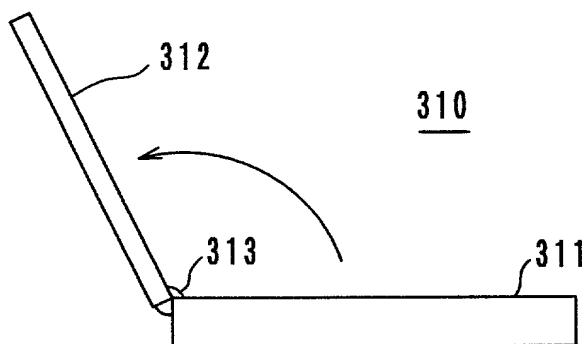
F / G. 16

310

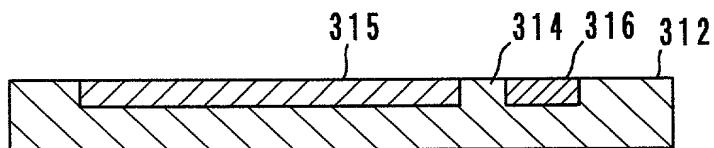


F / G. 17

310

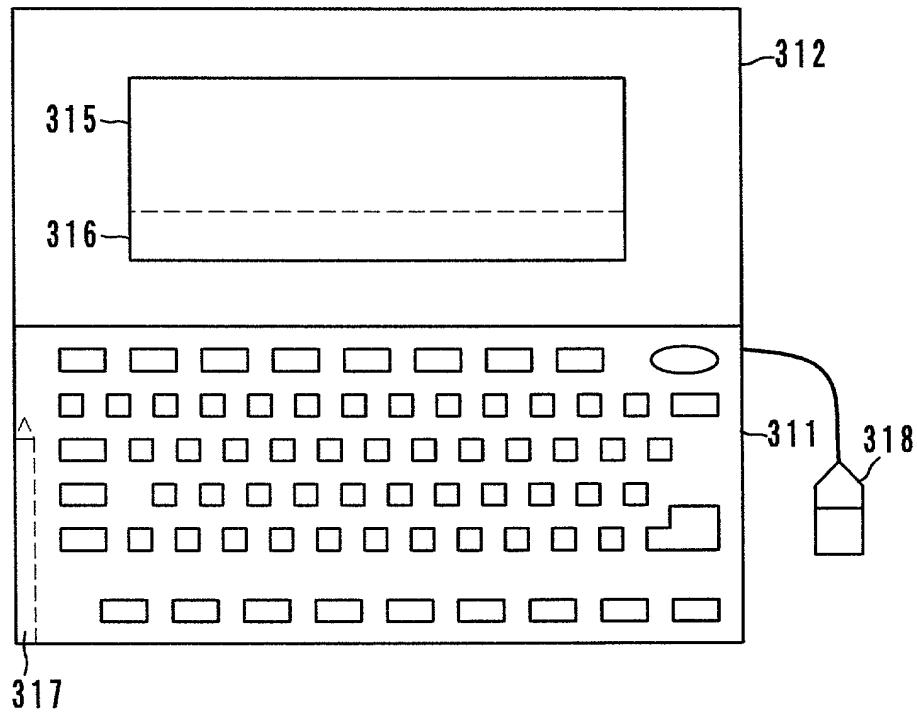


F / G. 18



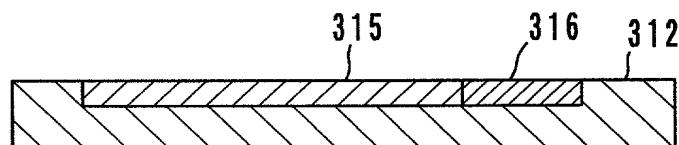
*F / G. 19*

310

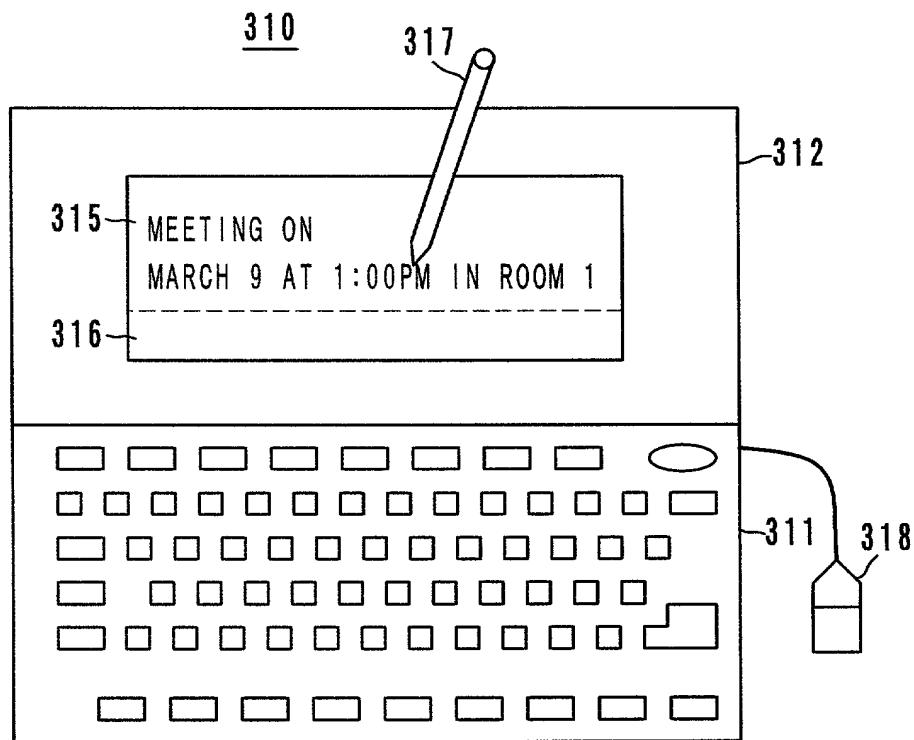


006240 "E444560

*F / G. 20*



F / G . 2 1



F / G. 22 a

DESIGNATION OF  
PART TO BE CHANGED

317

MEETING ON  
MARCH 9 AT 1:00PM IN ROOM 1

315

316

F / G. 22 b

DESIGNATION OF  
LETTERS TO BE CHANGED

317

MEETING ON  
MARCH 9 AT 1:00PM IN ROOM 1

315

316

F / G. 22 c

REVISION

317

MEETING ON  
MARCH 9 AT 1:00PM IN ROOM 1

315

316

F / G. 22 d

COMPLETION OF REVISION

MEETING ON  
MARCH 9 AT 1:00PM IN ROOM 2

315

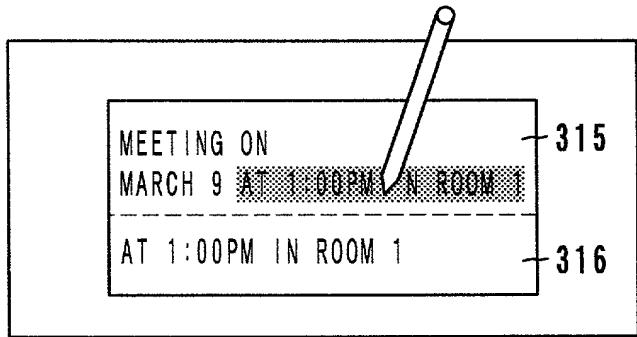
316

CONFIDENTIAL

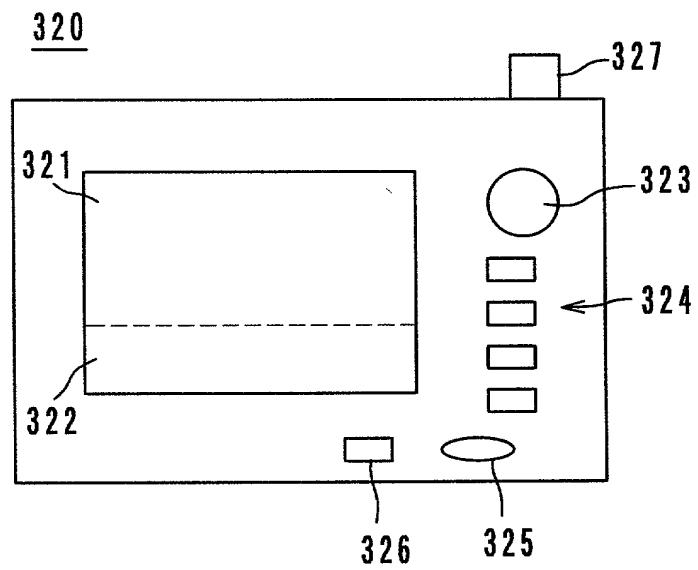
38

42

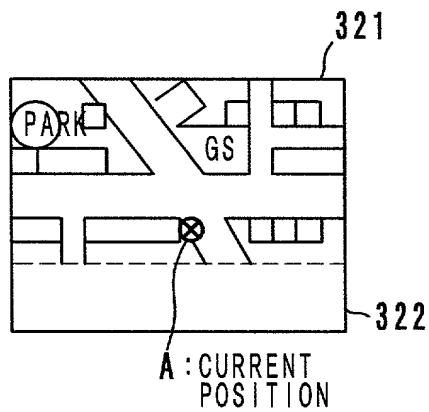
F / G. 23



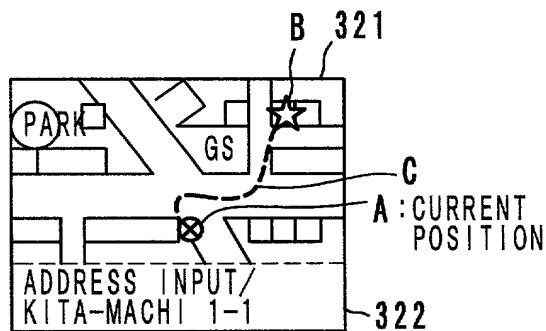
*F / G. 24*



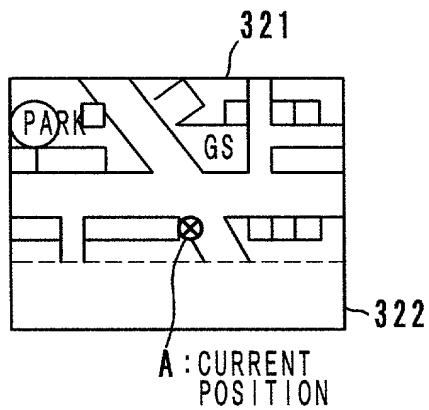
F / G. 25 a



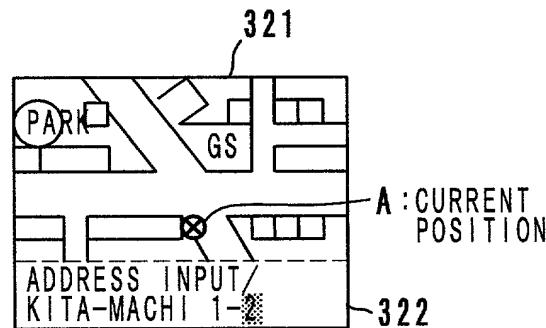
F / G. 25 b



F / G. 26 a

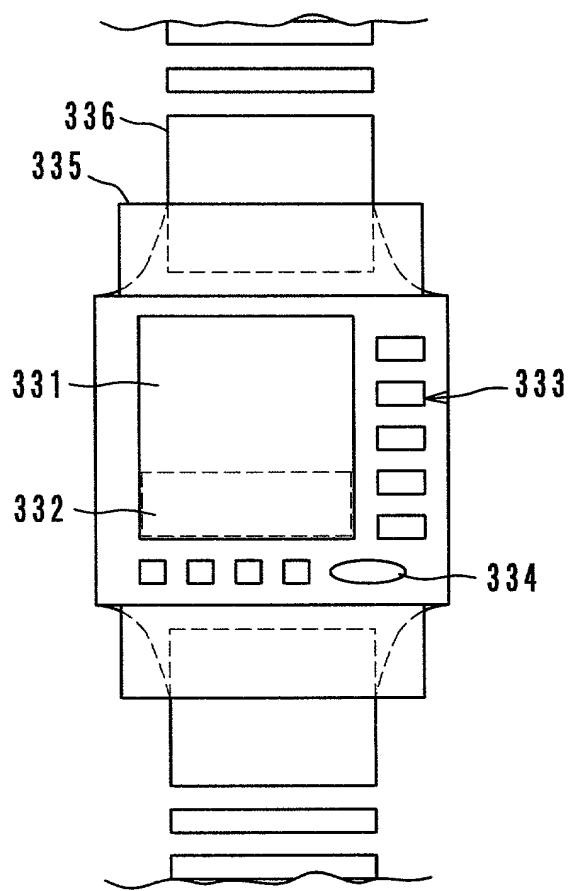


F / G. 26 b

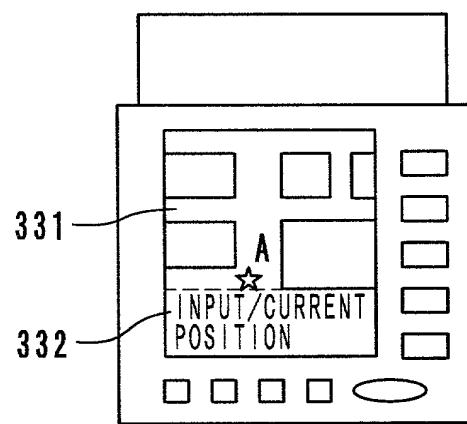


F / G. 27

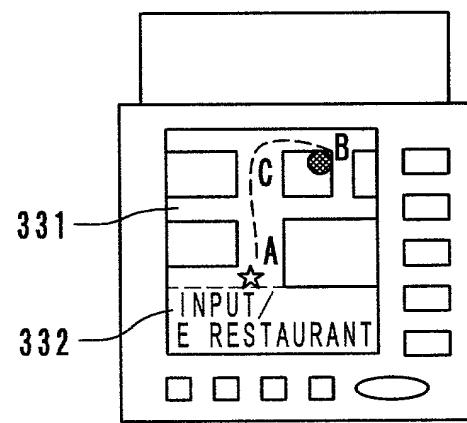
330



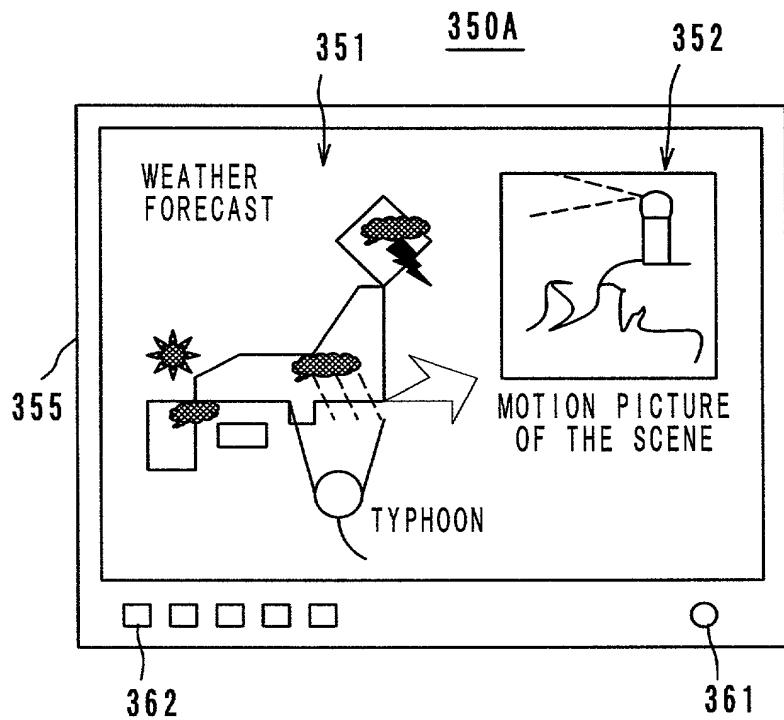
F / G. 28 a



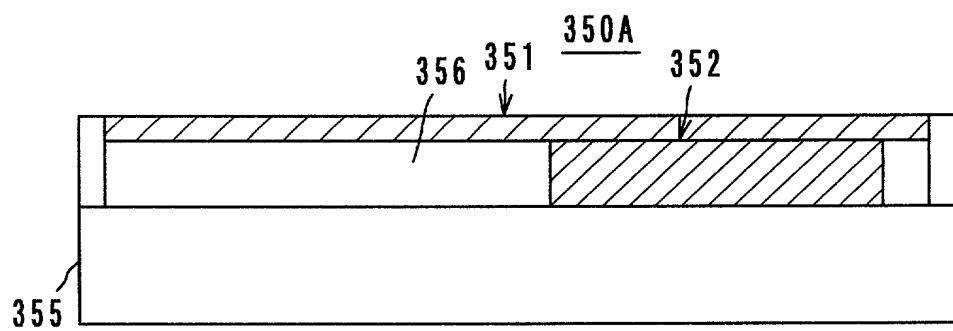
F / G. 28 b



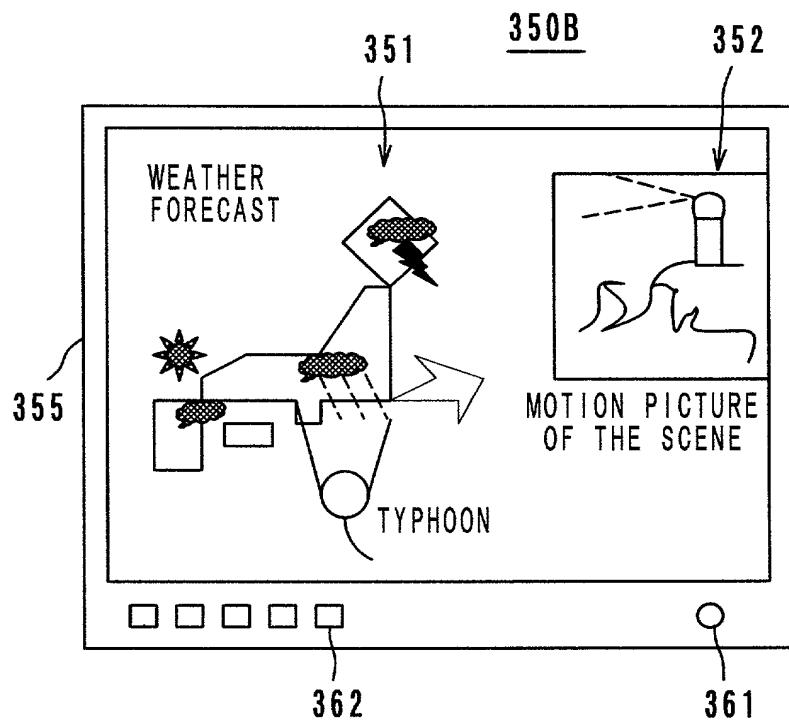
F / G. 29



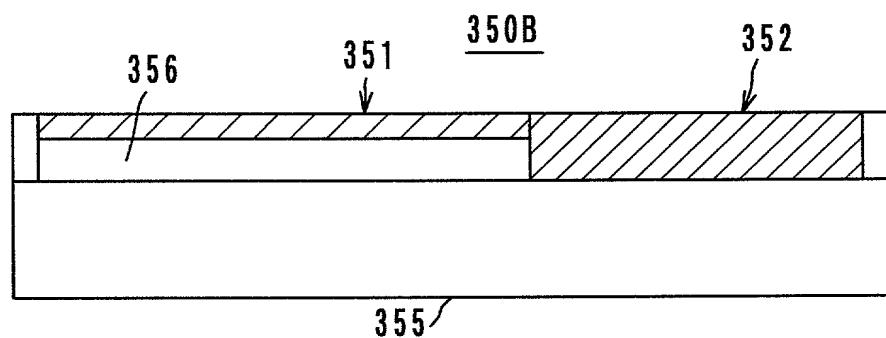
F / G. 30



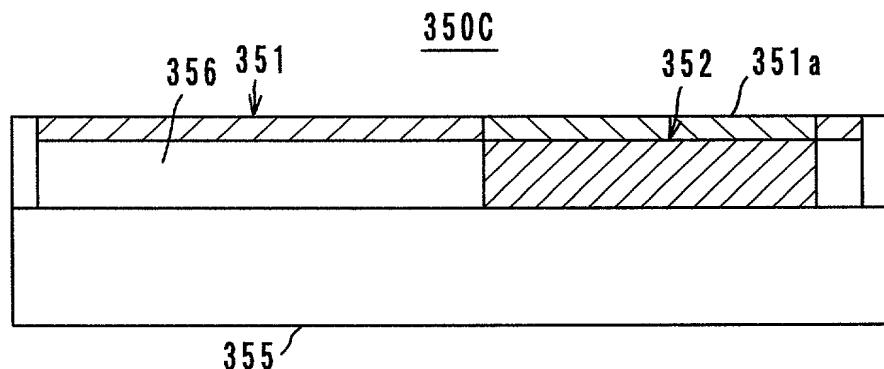
F / G. 31



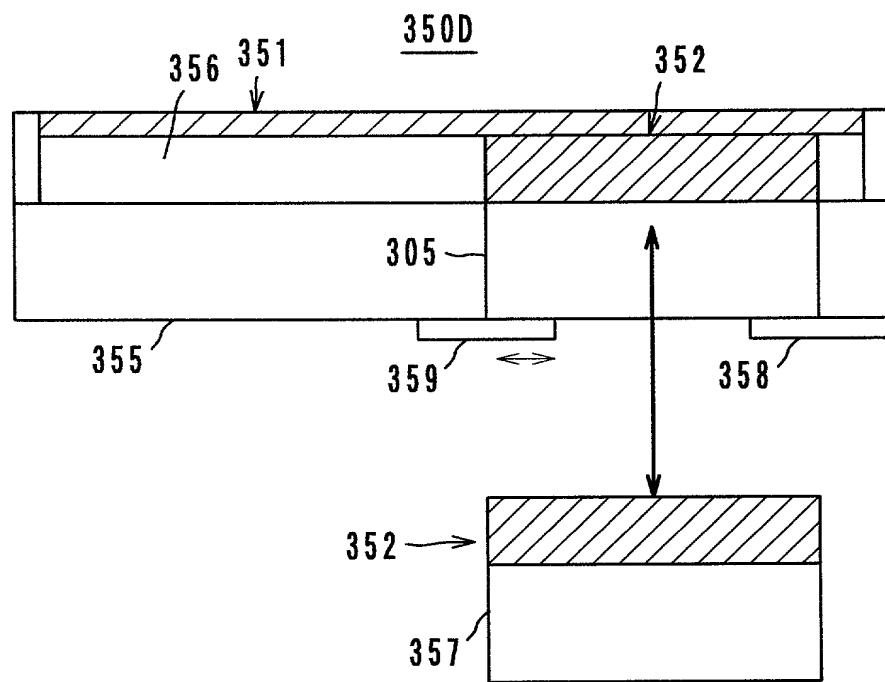
F / G. 32



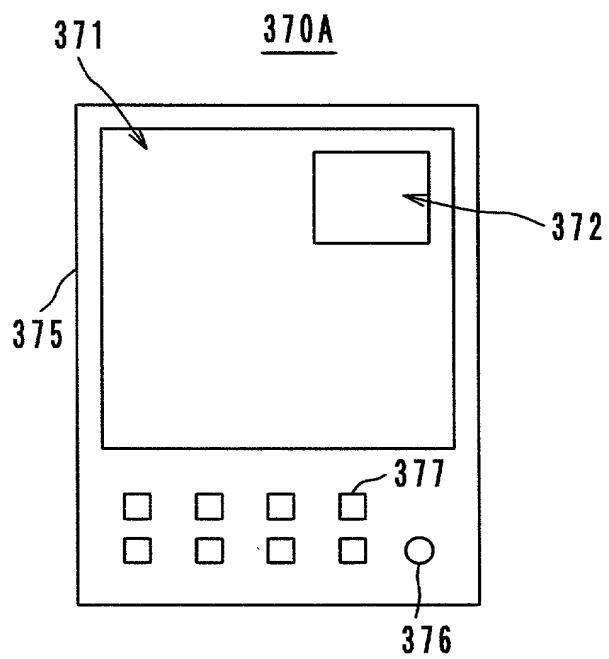
F / G. 33



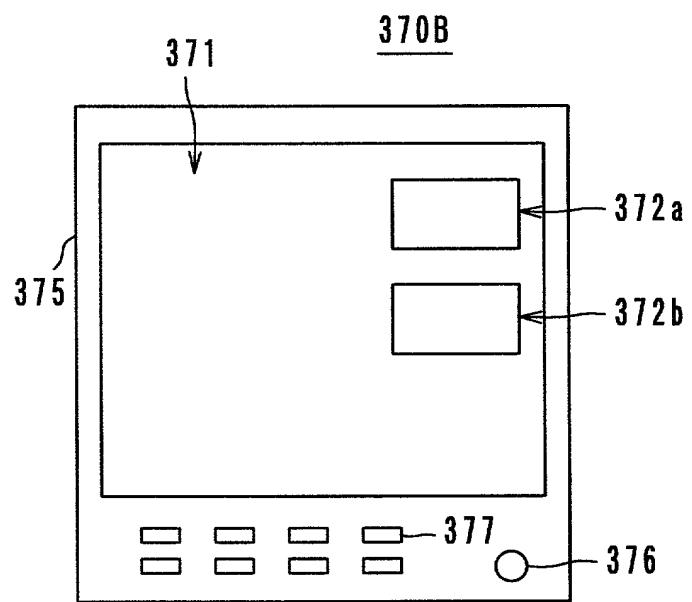
F / G. 34



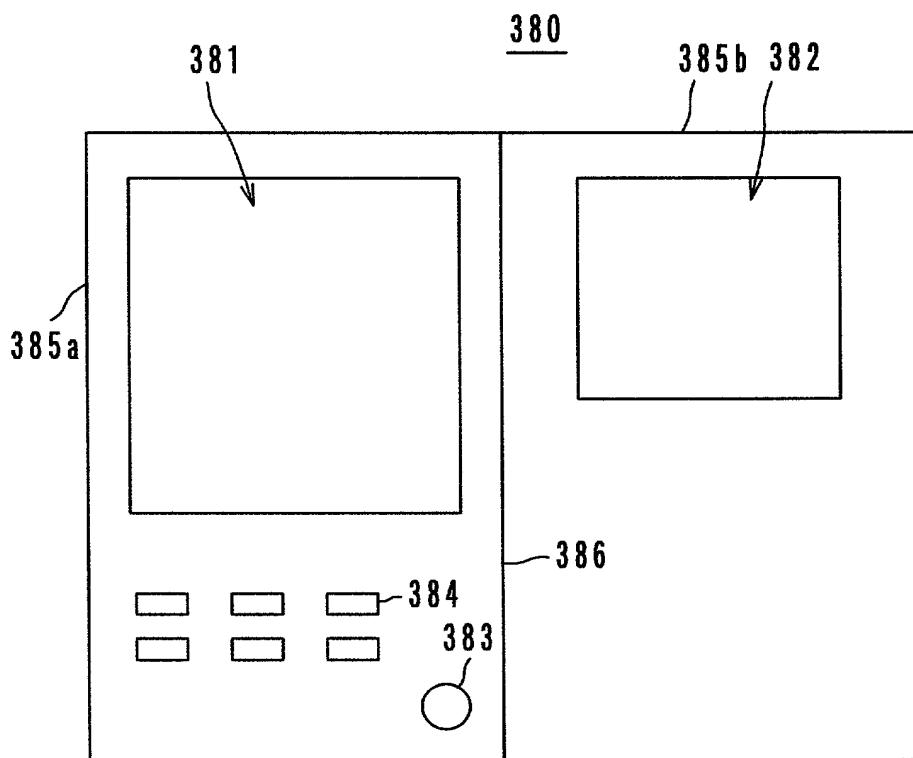
F / G. 35



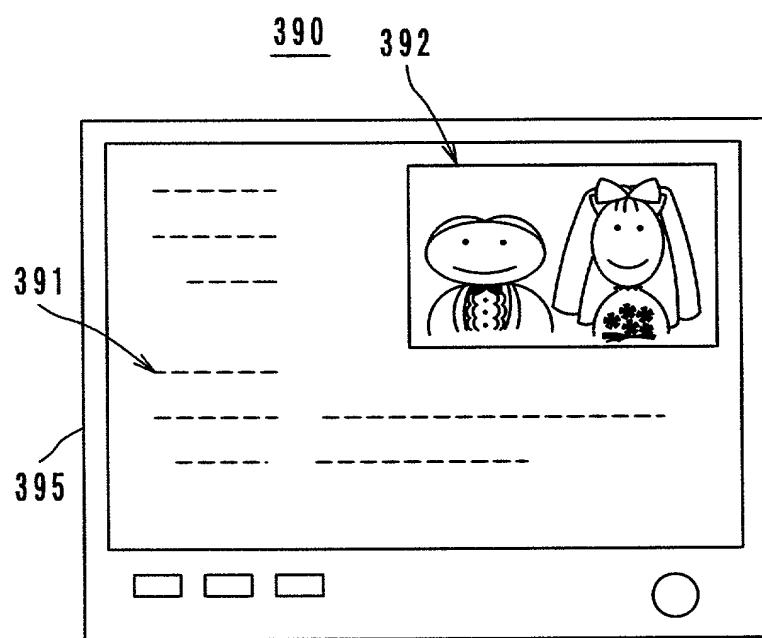
F / G. 36



F / G. 37

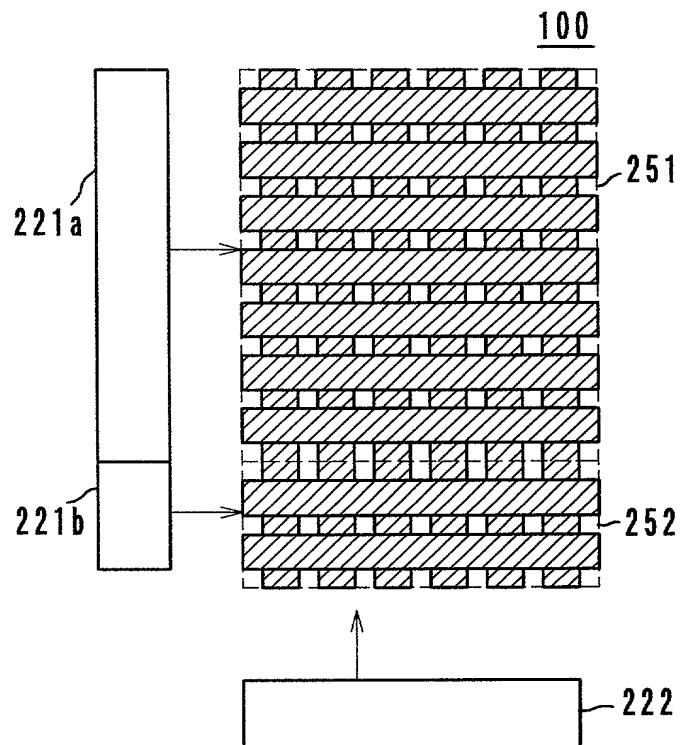


F / G. 38

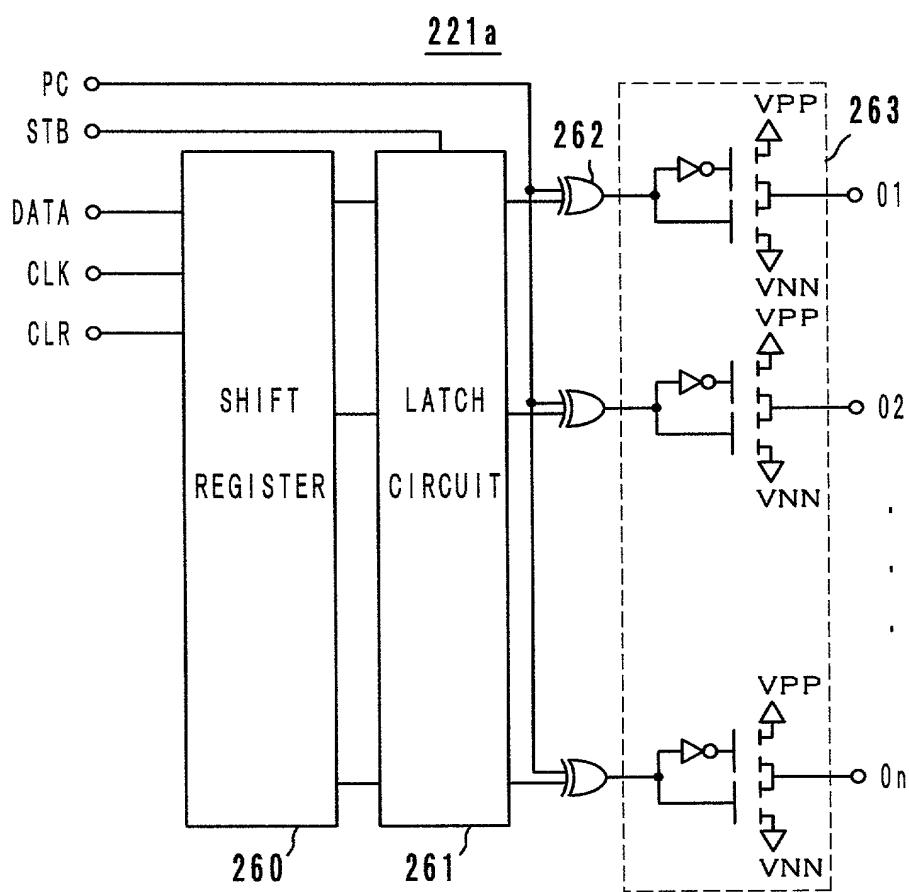


F / G. 39

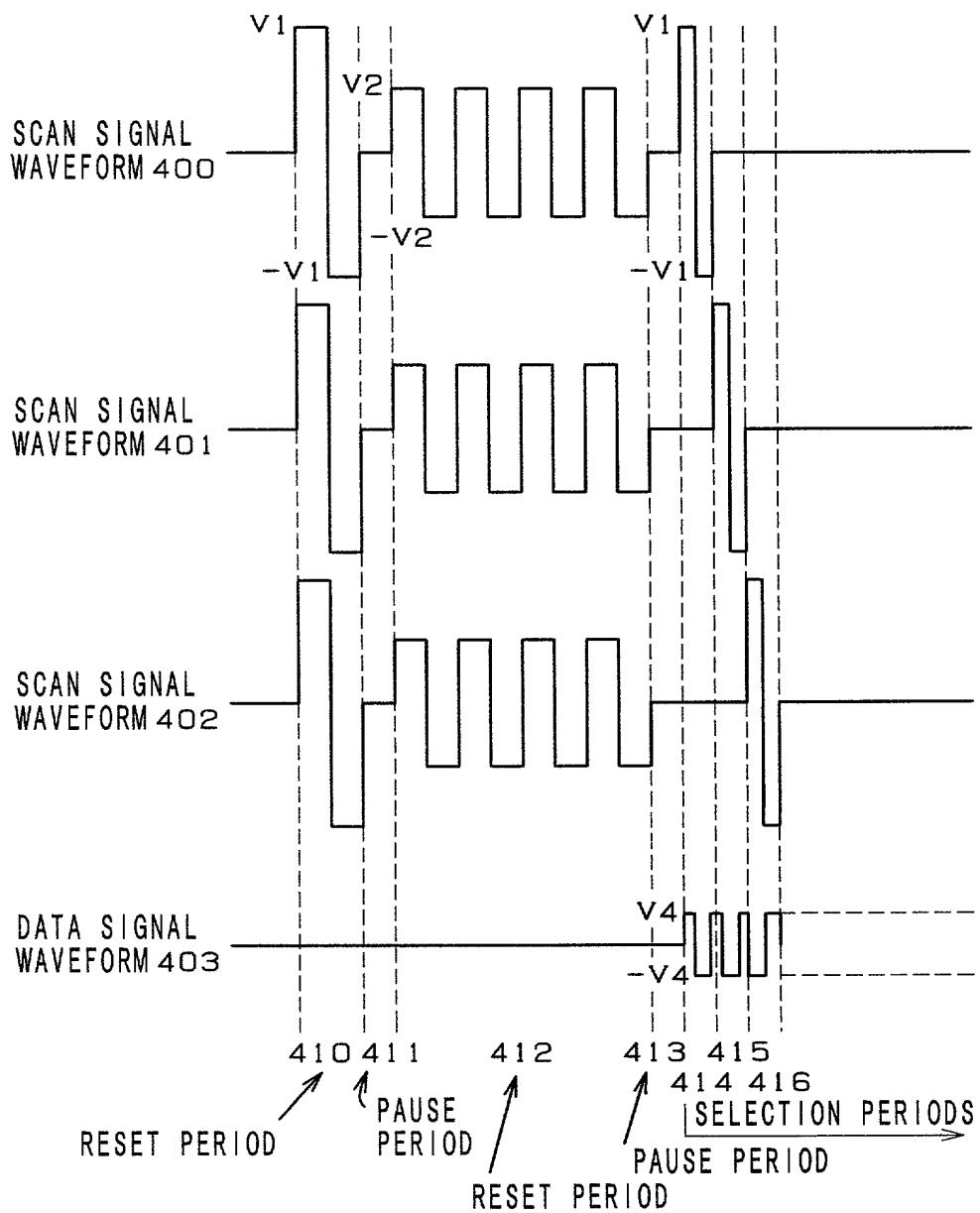
0062610-197400000



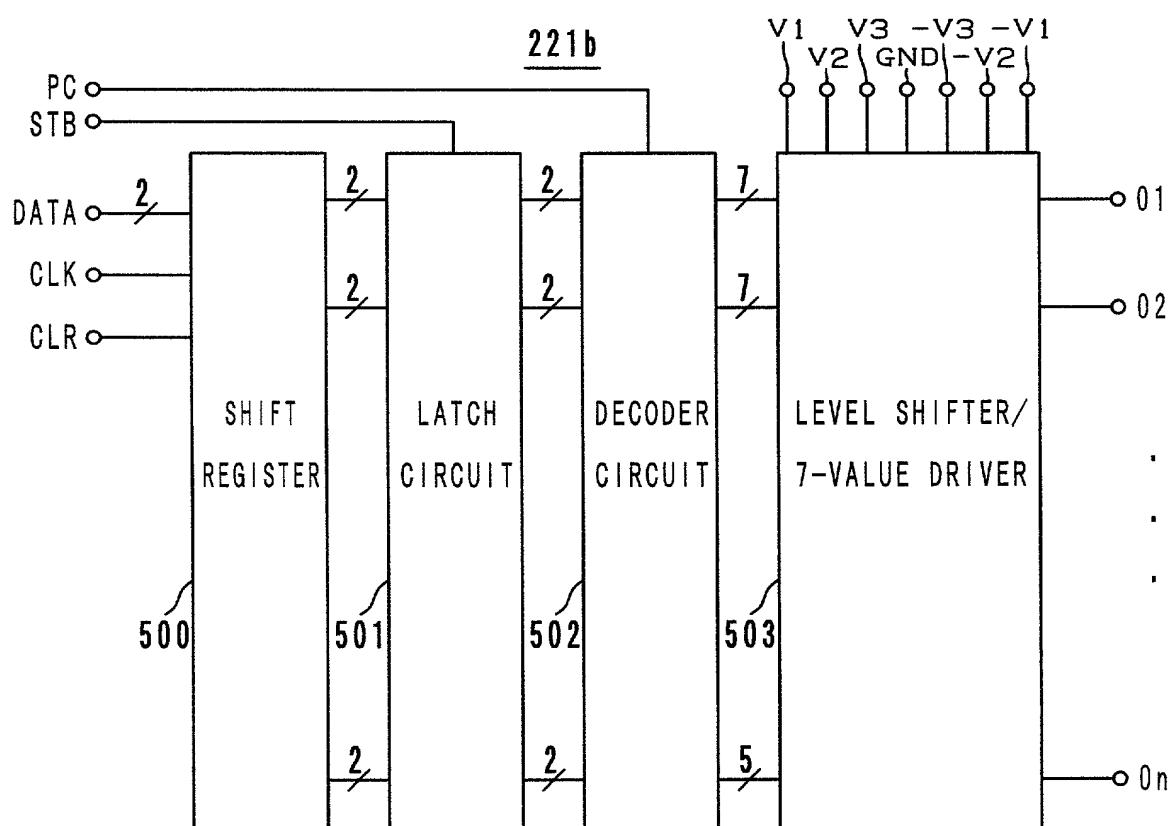
F / G. 40



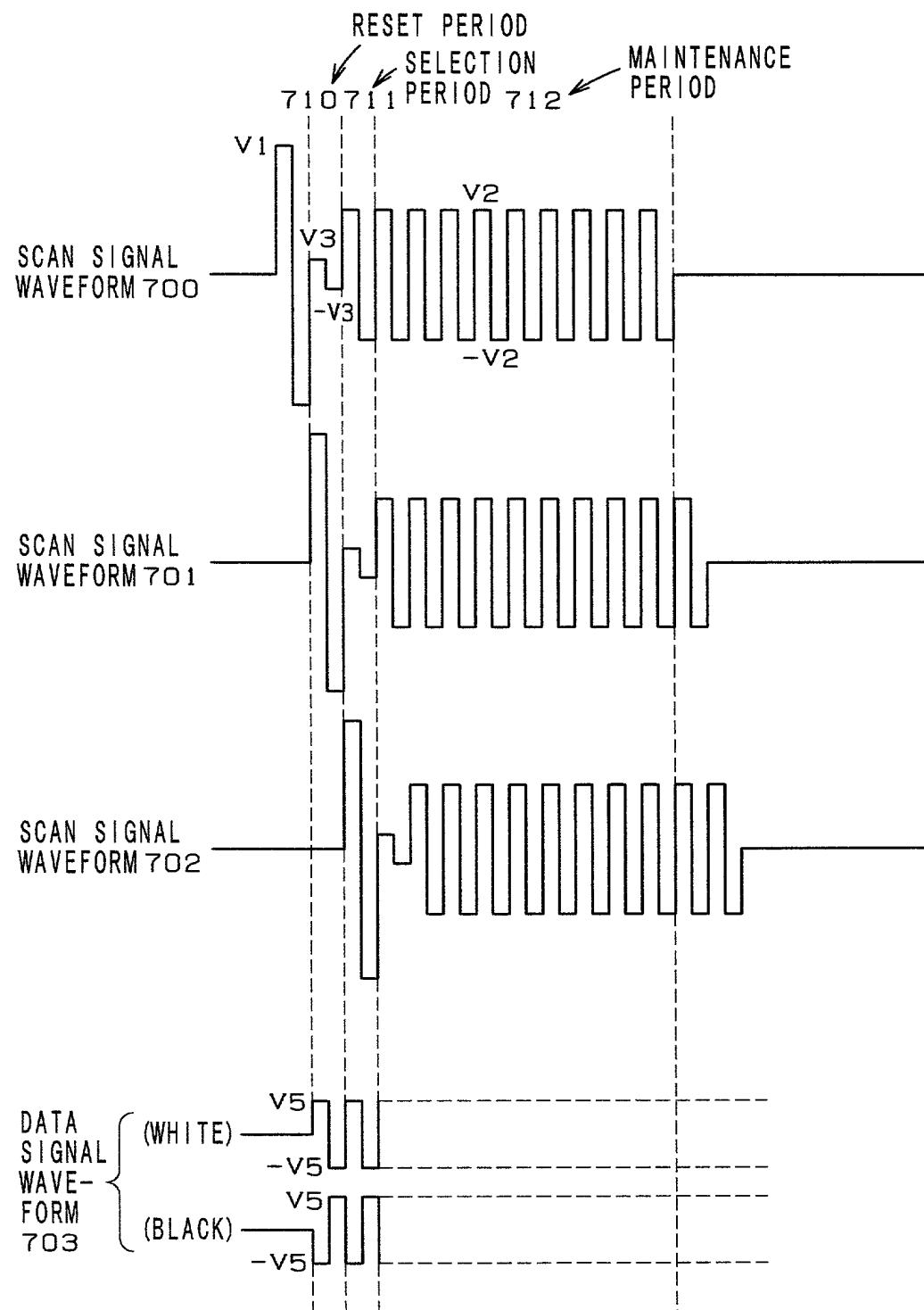
F / G. 41



F / G. 42

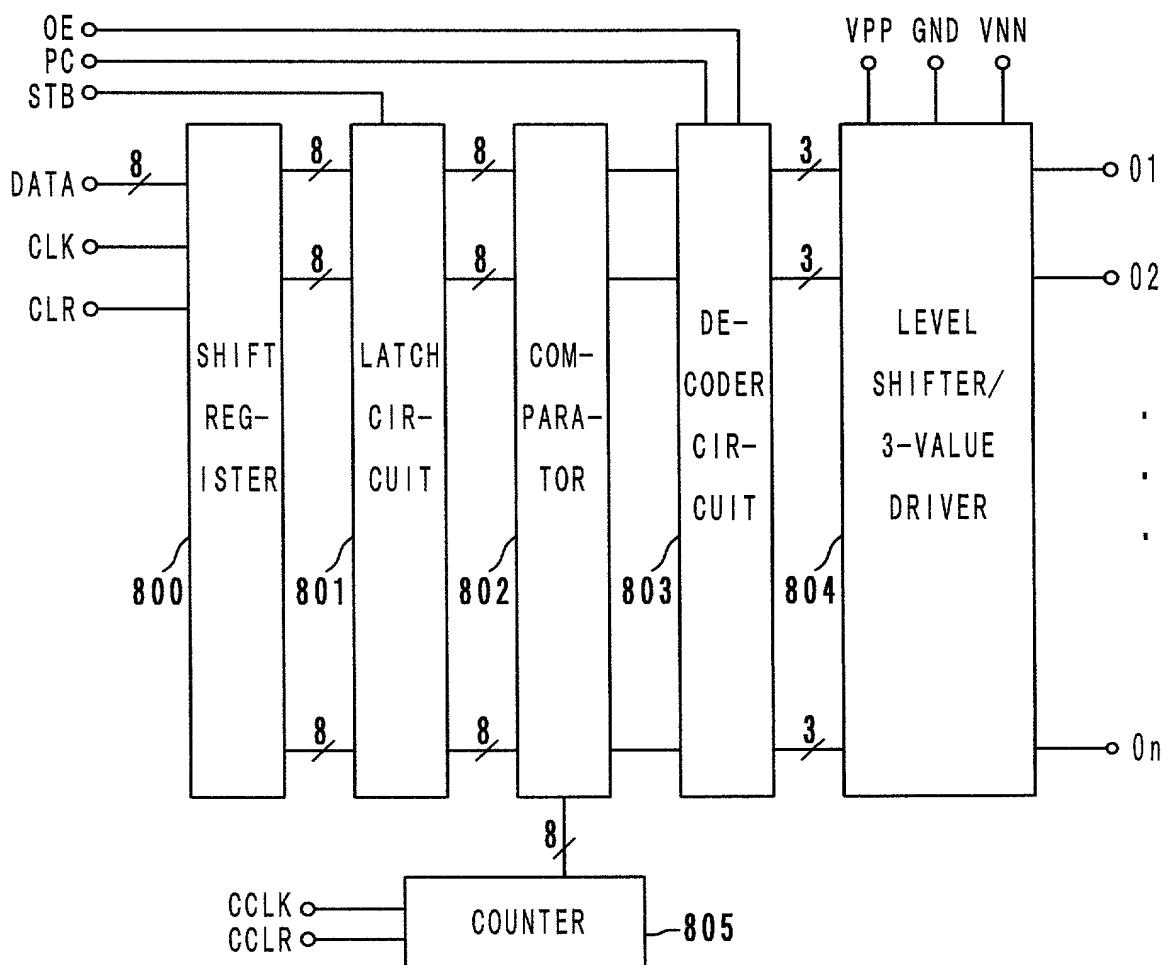


F / G. 43

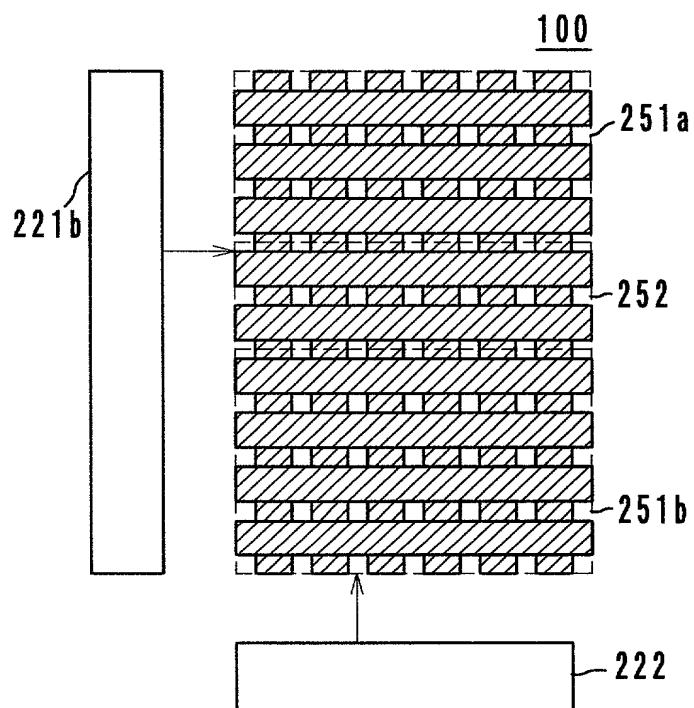


*F / G. 44*

222



F / G. 45



00062810 - 222222222220

F / G. 46 a

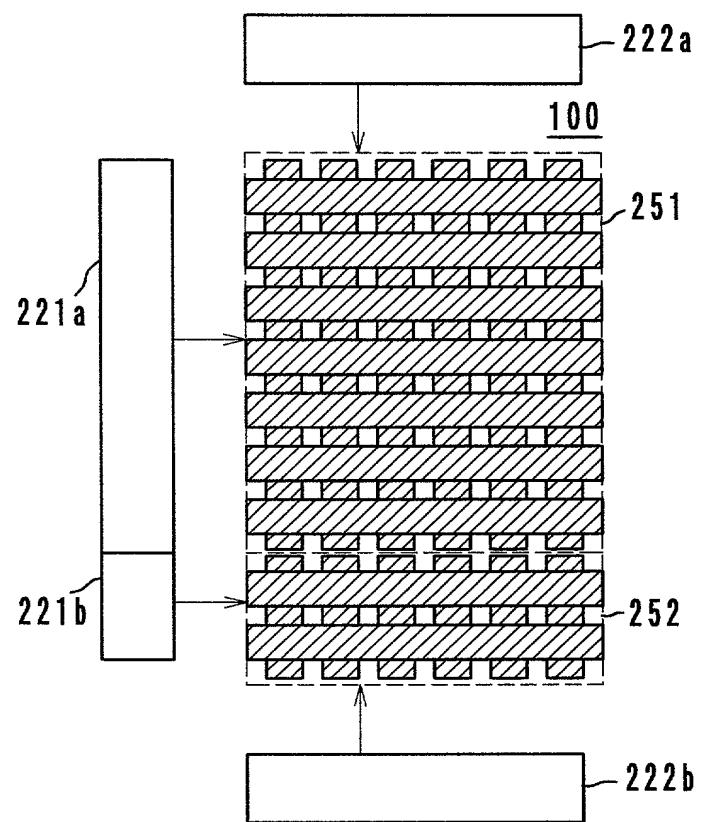
XXX, XXXXXX  
XXXXXX.  
XXXXXX, XX  
XXXXXXXXXX  
XXXXXXXXXX  
XX.  
XXXXXXXXXX  
XXXX.

F / G. 46 b

XXX, XXXXXX  
XXXXXX.  
X BOOKMARK XX  
XX SEARCH XX  
XX PAGE JUMP XX  
XX.  
XXXXXXXXXX  
XXXX.

253

*F / G. 47*



**DECLARATION AND POWER OF ATTORNEY  
FOR PATENT APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name.

I believe I and the other persons listed below are the original, first, and joint inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled

**INFORMATION DISPLAY APPARATUS**

the specification of which is attached hereto unless the following box is checked:

was filed on \_\_\_\_\_  
as United States Application Number or  
PCT International Application Number  
\_\_\_\_\_ and was amended on  
\_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority under Title 35, United States Code, Section 119(a)-(d) or 365(b) of any foreign applications for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

PRIOR FOREIGN APPLICATIONS

Priority  
Not Claimed

11-093007 (Number)	JAPAN (Country)	31 March 1999 (Day/Month/Year Filed)
11-161939 (Number)	JAPAN (Country)	9 June 1999 (Day/Month/Year Filed)
11-279074 (Number)	JAPAN (Country)	30 September 1999 (Day/Month/Year Filed)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

Li Chen, Reg. No. 46,284  
Charles S. Cotropia, Reg. No. 27,189  
Kathi A. Cover, Reg. No. 37,803  
Daren C. Davis, Reg. No. 38,425  
Mark A. Dodd, Reg. No. 45,729  
Barry W. Dove, Reg. No. 45,862  
David L. Hitchcock, Reg. No. 30,067  
Kelly J. Kubasta, Reg. No. 44,196  
William D. McSpadden, Reg. No. 44,234  
Stephen B. Perkins, Reg. No. 45,009  
Steven P. Rhines, Reg. No. 38,595  
Thomas N. Tarnay, Reg. No. 41,341  
James W. Williams, Reg. No. 20,047

Send correspondence to: Direct telephone calls to:

SIDLEY & AUSTIN  
717 North Harwood  
Suite 3400  
Dallas, Texas 75201-6507

James W. Williams  
\_\_\_\_\_  
Direct: (214) 981-3328  
Main: (214) 981-3300  
Attorney Docket No.: 15162/01740

Full name of first joint inventor:

Yoshifumi SHIBATA  
First Middle Last

Inventor's signature:

Date: \_\_\_\_\_

Citizenship: JAPAN

Post Office Address: c/o MINOLTA CO., LTD.

---

Osaka Kokusai Building, 3-13, 2-Chome,

Azuchi-Machi, Chuo-Ku, Osaka-Shi

Osaka 541-8556 JAPAN

Full name of second joint inventor:

Kiyofumi HASHIMOTO  
First Middle Last

Inventor's signature: \_\_\_\_\_

Date: \_\_\_\_\_

Residence: Osaka-Shi Osaka JAPAN  
(city) (prefecture) (country)

Citizenship: JAPAN

Post Office Address: c/o MINOLTA CO., LTD.

Osaka Kokusai Building, 3-13, 2-Chome,

Azuchi-Machi, Chuo-Ku, Osaka-Shi

Osaka 541-8556 JAPAN

Full name of third joint inventor:

Eiji YAMAKAWA  
First Middle Last

Inventor's signature: \_\_\_\_\_

Date: \_\_\_\_\_

Residence: Osaka-Shi Osaka JAPAN  
(city) (prefecture) (country)

Citizenship: JAPAN

Post Office Address: c/o MINOLTA CO., LTD.

Osaka Kokusai Building, 3-13, 2-Chome,

Azuchi-Machi, Chuo-Ku, Osaka-Shi

Osaka 541-8556 JAPAN

Full name of fourth joint inventor:

Hideo HOTOMI  
First Middle Last

Inventor's signature: \_\_\_\_\_

Date: \_\_\_\_\_

Residence: Osaka-Shi Osaka JAPAN  
(city) (prefecture) (country)

Citizenship: JAPAN

Post Office Address: c/o MINOLTA CO., LTD.

Osaka Kokusai Building, 3-13, 2-Chome,

Azuchi-Machi, Chuo-Ku, Osaka-Shi

Osaka 541-8556 JAPAN

00000000000000000000000000000000

Full name of fifth joint inventor:

Naoki MASAZUMI  
First Middle Last

Inventor's signature: \_\_\_\_\_

Date: \_\_\_\_\_

Residence: Osaka-Shi Osaka JAPAN  
(city) (prefecture) (country)

Citizenship: JAPAN

Post Office Address: c/o MINOLTA CO., LTD.

Osaka Kokusai Building, 3-13, 2-Chome,

Azuchi-Machi, Chuo-Ku, Osaka-Shi

Osaka 541-8556 JAPAN

Full name of sixth joint inventor:

Koichi KOHRIYAMA  
First Middle Last

Inventor's signature:

Date: \_\_\_\_\_

Citizenship: JAPAN

Post Office Address: c/o MINOLTA CO., LTD.

Osaka Kokusai Building, 3-13, 2-Chome,

---

Azuchi-Machi, Chuo-Ku, Osaka-Shi

Osaka 541-8556 JAPAN